## / Descriptions

WR0553I ṂṂ Ł SQS ÆṀ ч s ‖ ㄱ Silicon PNP transistor in a TO-220F Plastic Package.

## / Features

kIH ㅓ ó Ḅ/ль NWG5393 Ӣ ´

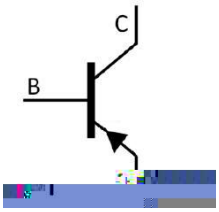
Good Linearity of $h_{FE}$, complementary pair with KTD2060.

## / Applications

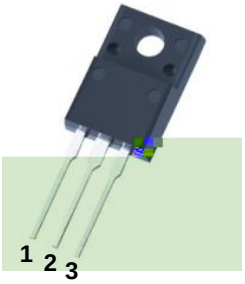
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General Purpose Application.

## / Equivalent Circuit




## / Pinning




PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

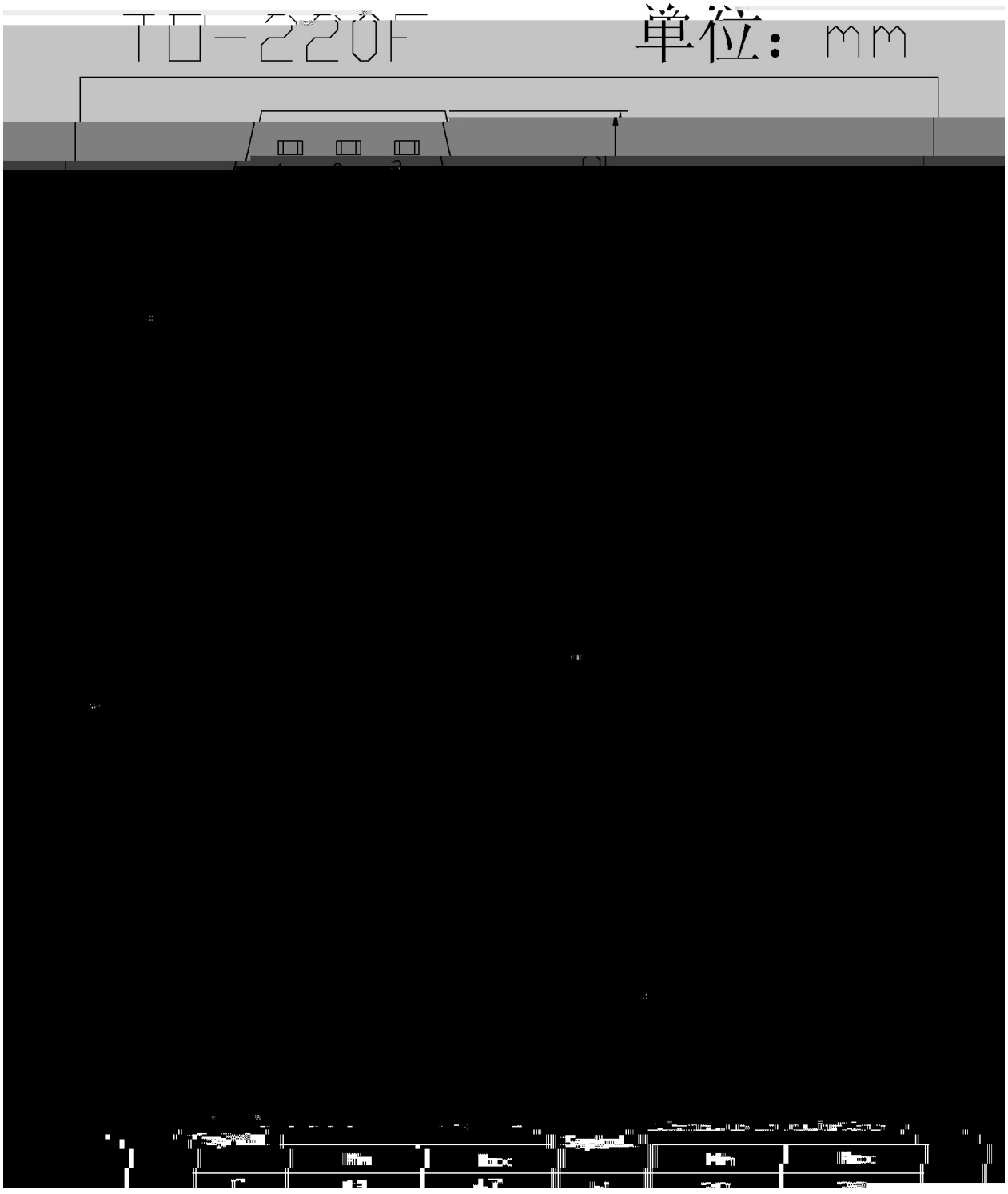
## / $h_{FE}$ Classifications & Marking

| $h_{FE}$ Classifications Symbol | R | O | Y |
|---|---|---|---|
| $h_{FE}$ Range | 40~80 | 70~140 | 120~240 |

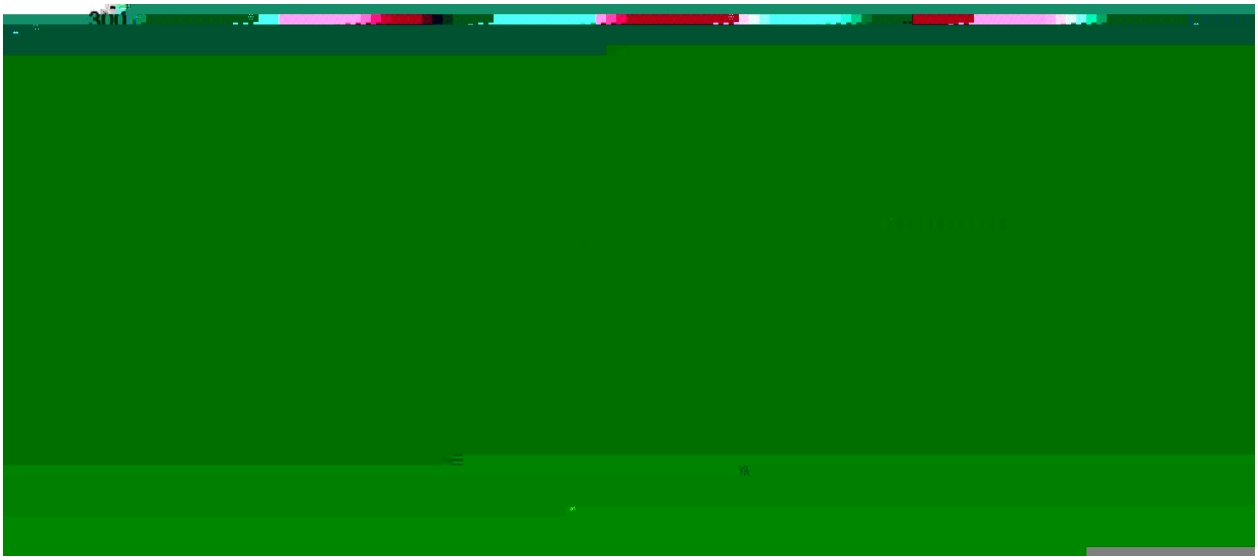
| v ‴Ω<br>Parameter | ϒ N<br>Symbol | ‴Ω Ö<br>Rating | D Ђ<br>Unit |
|---|---|---|---|
| Collector to Base Voltage | $V_{CBO}$ | -80 | V |
| Collector to Emitter Voltage | $V_{CEO}$ | -80 | V |
| Emitter to Base Voltage | $V_{EBO}$ | -5 | V |
| Collector Current - Continuous | $I_C$ | -4 | A |
| Base Current | $I_B$ | -0.4 | A |
| Collector Power Dissipation | $P_C$(Tc=25 ) | 25 | W |
| Junction Temperature | $T_j$ | 150 | ł |
| Storage Temperature Range | $T_{stg}$ | -55 Ќ 150 | ł |

| v ‴Ω<br>Parameter | ϒ N<br>Symbol | ╨ :┆ ţ<br>Test Conditions | ώ ŋ Ö<br>Min | Ɛ ʒ Ö<br>Typ | ώ ᶻ Ö<br>Max |
|---|---|---|---|---|---|

## / Package Dimensions



## (  )  /  Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ů 25 Ḱ 150 ł ı ˋH ڑ 60 Ḱ 90sec;

2 Š Ө □ ů 255 ° 5 ł ı ˋH ڑ ˋY ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ﾘ ˛ ↄ ෯ ů ㎛ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.