

BRDTC114EUAQ

Rev.A Apr.-2023

SOT-323

NPN

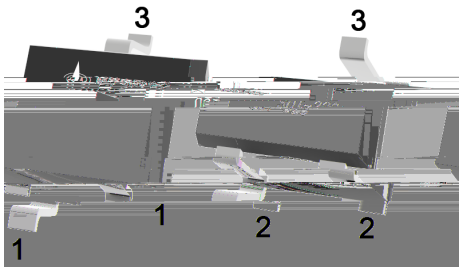
Silicon NPN Digital Transistor in a SOT-323 Plastic Package.

AEC-Q101

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process, Qualified to AEC-Q101 Standards for High Reliability, HF Product.

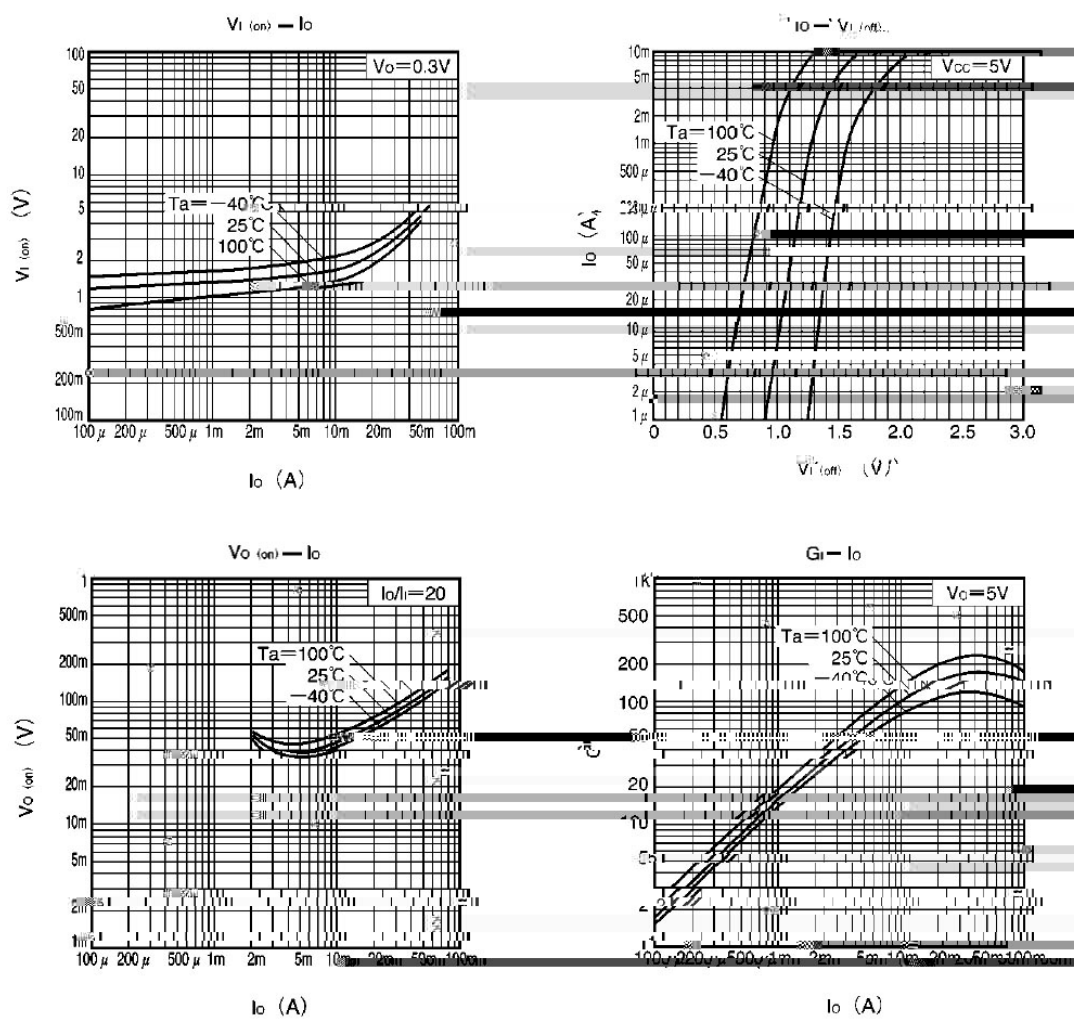
Switching, Inverter circuit, Interface circuit and driver circuit applications, Meet the stringent requirements of automotive applications.

/ Equivalent Circuit

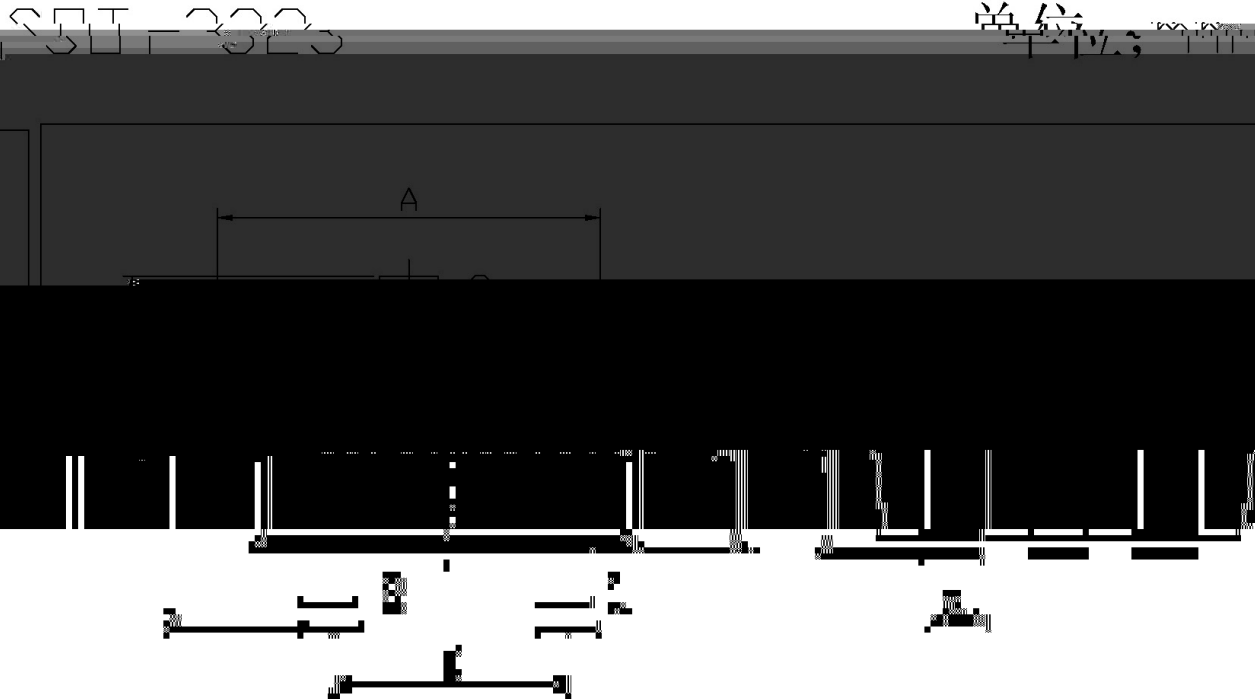
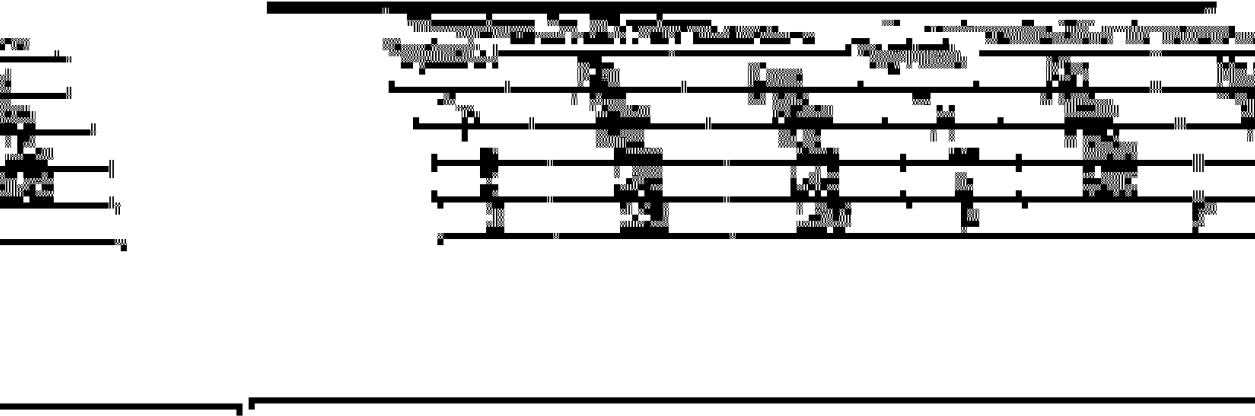


PIN1 Base PIN 2

/ Electrical Characteristic Curve



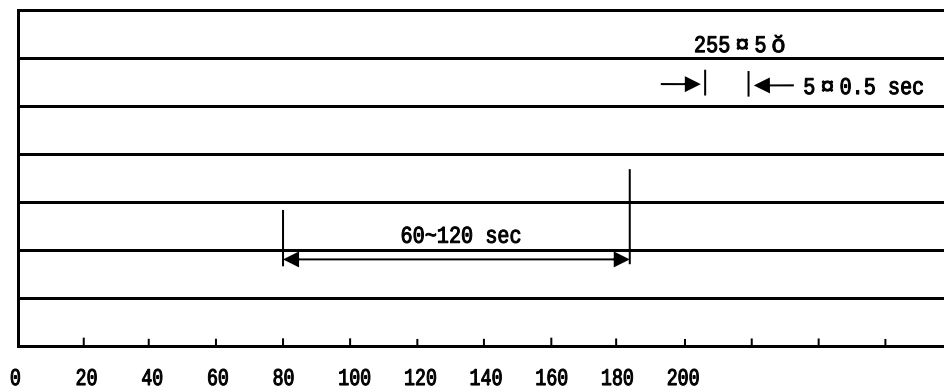
/ Package Dimensions



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() / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- 1 150 200 60 120sec; 1.Preheating:150~200 , Time:60~120sec.
- 2 255±5 5±0.5sec; 2.Peak Temp.:255±5 , Duration:5±0.5sec.
- 3 2 10 /sec. 3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

260±5

10±1 sec.

Temp:260±5

Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type	Units					Dimension (unit mm ³)		
	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box
SOT-323	3,000	10	30,000	6	180,000	7 ρ ×8	180×120×180	390×385×205

/ Notices