"Yت / Descriptions

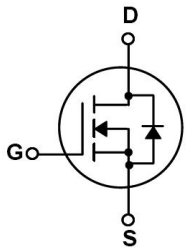
◕ È / Features

$R_{DS(on)}$ C_{rss} AEC-Q101

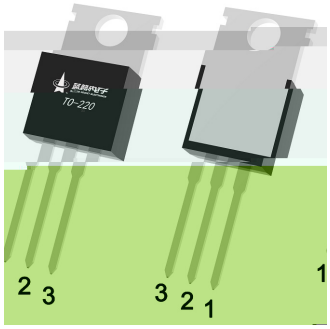
٧ ڍ / Applications

DC/DC

Л ف ڎ ڑ ≠ ع / Equivalent Circuit



ã ﻷ ù ˜ / Pinning

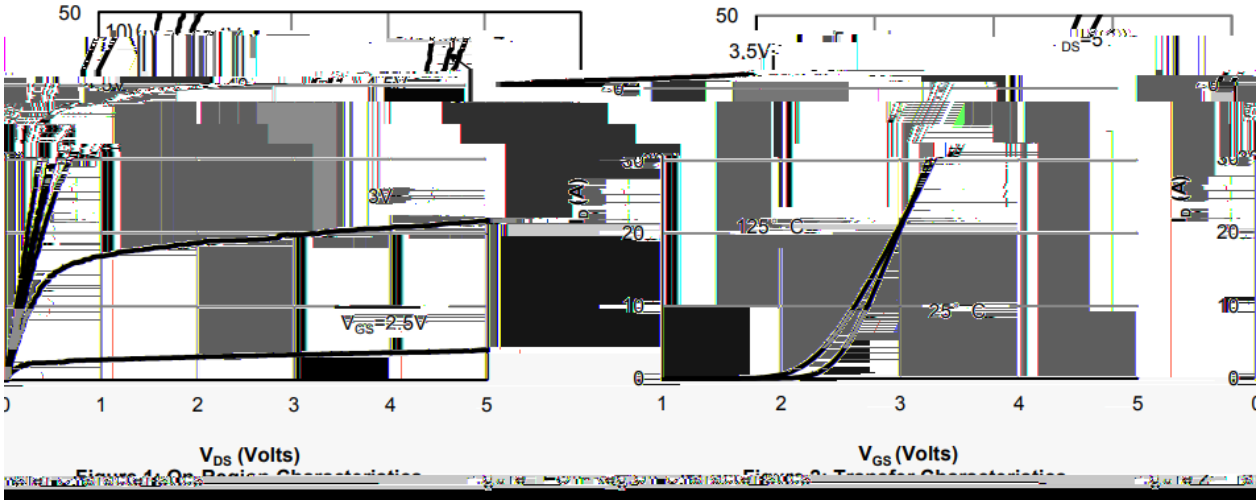


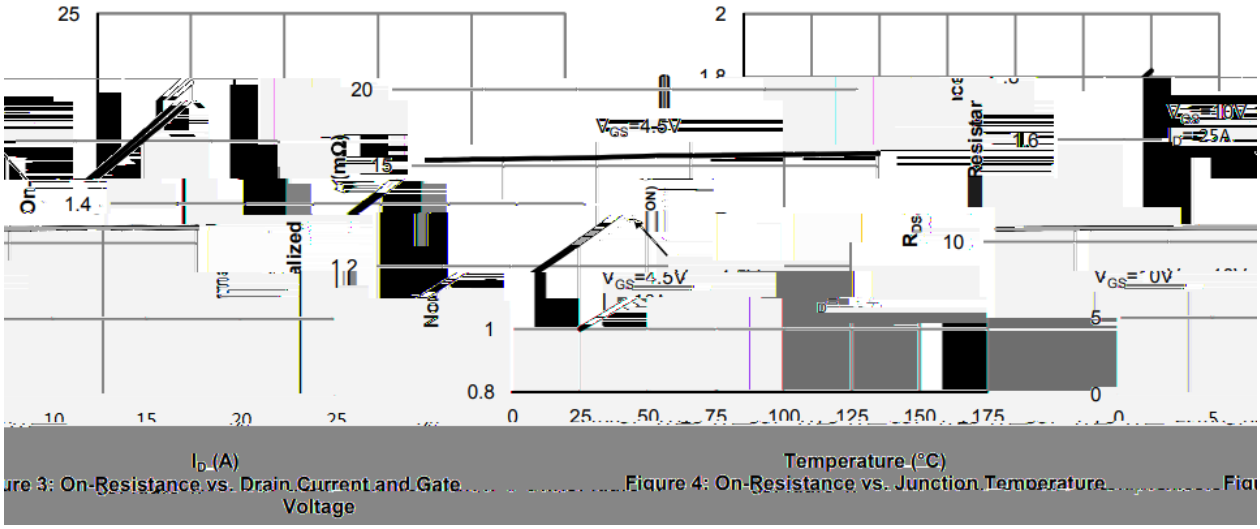
ł . ʜ ڍ / Marking

≠ ∧ ∃ Ã / Electrical Characteristics(Ta=25 ⊿)

		Ω q				

≠ ∃ Ã´E ∀ ƒ / Electrical Characteristic Curve





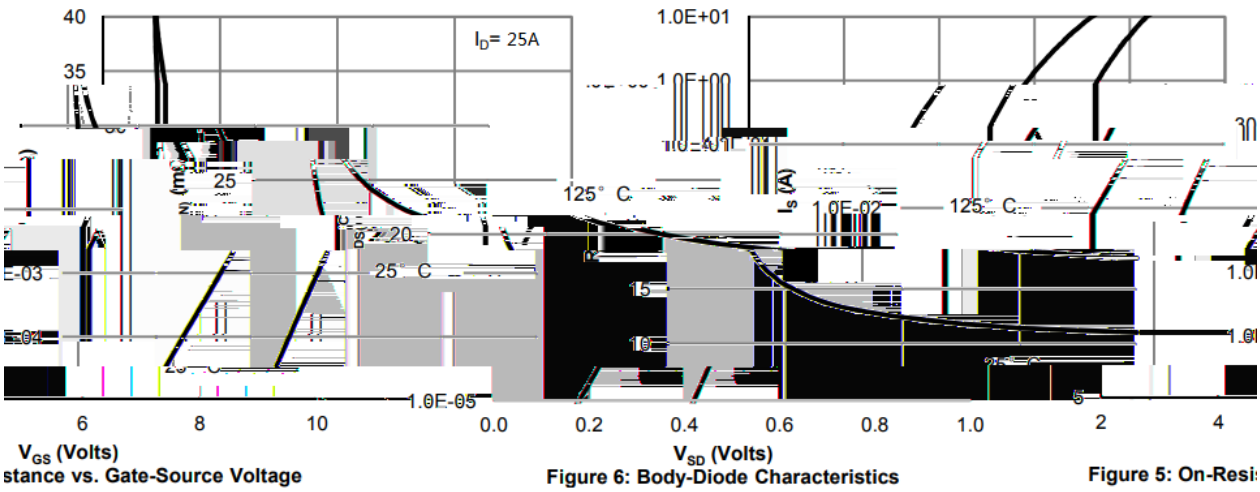
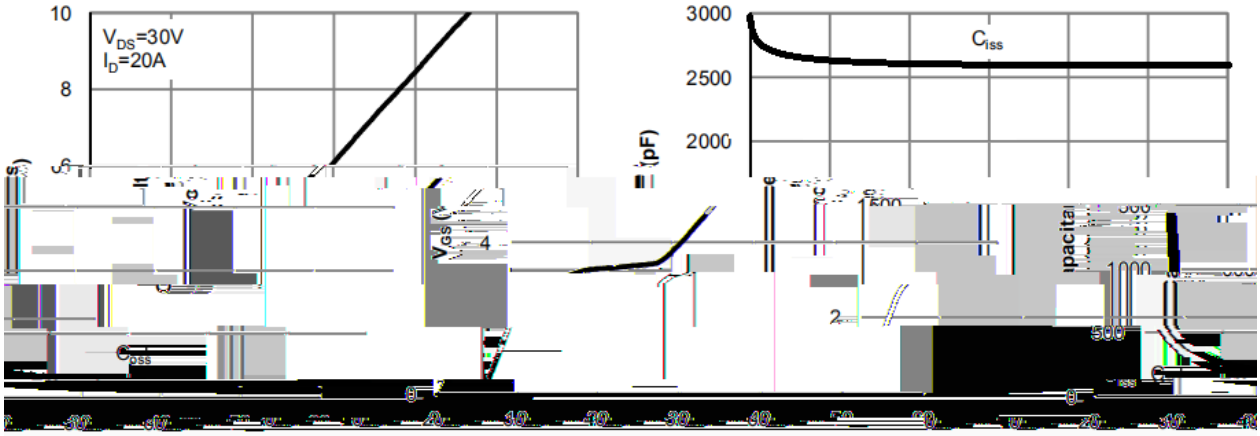


Figure 5: On-Resi

Figure 6: Body-Diode Characteristics

≠ ∃ ÃΈ ∀ ƒ / Electrical Characteristic Curve



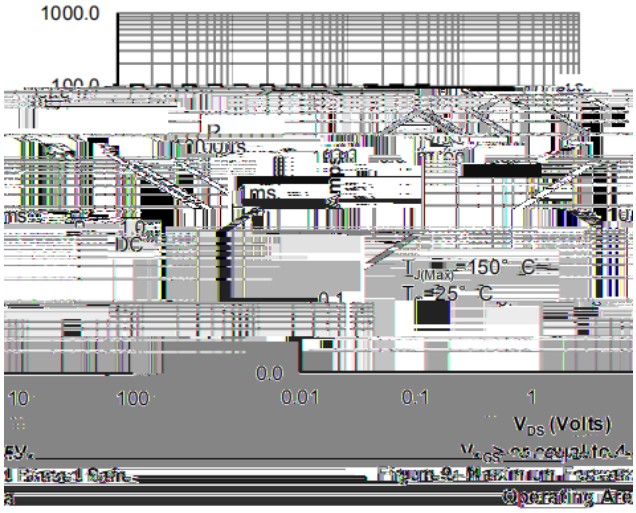
Q_g (nC)

Figure 7: Gate-Charge Characteristics

V_{DS} (Volts)

Figure 8: Capacitance Charac

teristics



Pulse Width (s)

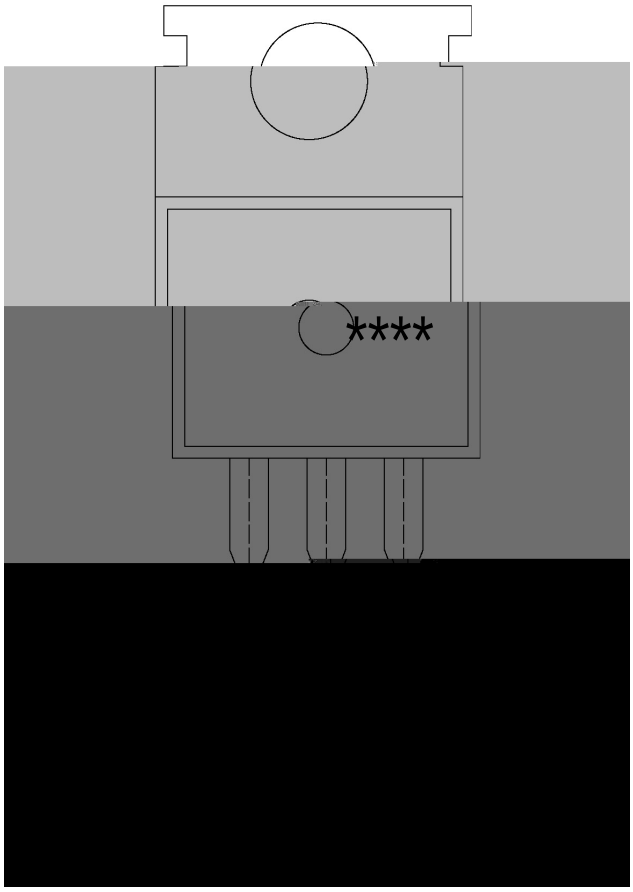
Figure 10: Normalized Maximum Transient Thermal Impedance

ᵘ ĖÒĻ ʃ / Package Dimensions

[redacted]

[redacted]

ł. Ļ'Ω / Marking Instructions

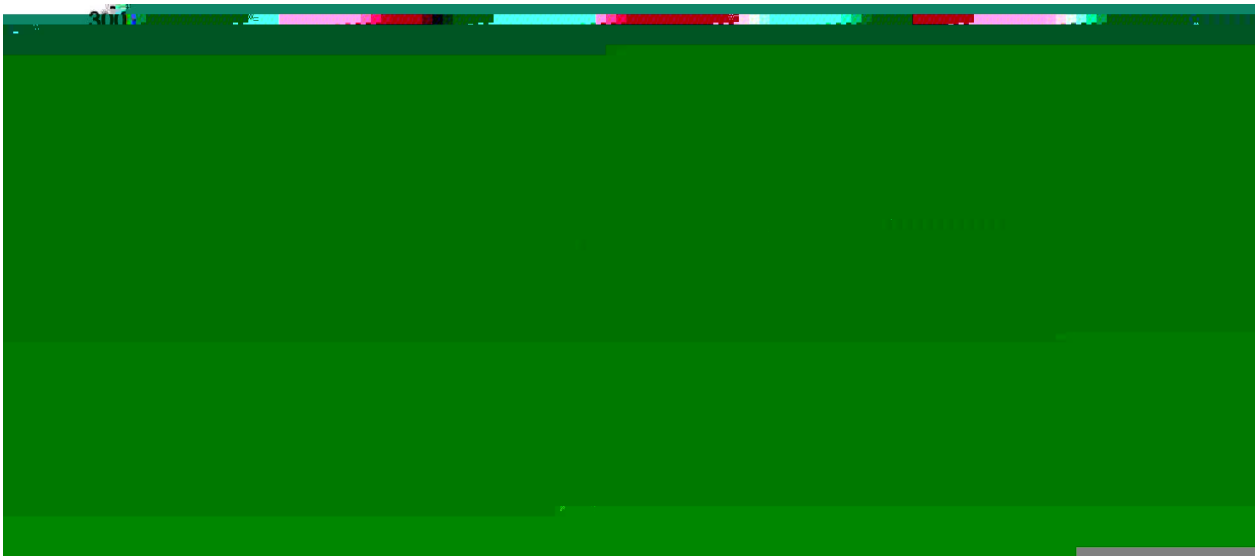


BR

50N06

****

↓ ș ╕ẃ´E ᛋ ƒ (ὼ ᴈ) / Temperature Profile for Dip Soldering(Pb-Free)



± ± ± ±

ㅓ ŭ ɋ ̄ `Y Ţ / Resistance to Soldering Heat Test Conditions

± ±

ɒ Ŀ ≮— / Packaging SPEC.

	Units/Bag /	Bags/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Bag	Inner Box	Outer Box

	Units/Tube /	Tubes/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Tube	Inner Box	Outer Box

ä ᴠ ŗ Ѽ / Notices