/ Descriptions

VRS0; Ṃ Ṃ Ŀ Q ǰ MOS ʃ ᾧ Ẕ ˥ N-Channel MOSFET in a SOP-8 Plastic Package.

/ Features

Ḥ Ṁ ڼ ɔ ڳ $U_{GV\ \varepsilon\ RQ\ \zeta}$ ı Ḥ ǁ ɔ U ı Ч ק˜ ȍ ھ چ ̈Ω ≠ ớ ı ˥ ᵀ UrKV

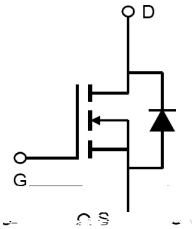
Low $R_{DS(ON)}$, Low Gate Charge, Optimized for fast-switching, RoHS.

/ Applications

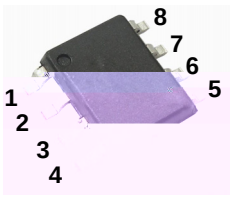
GF2GF ⊃ DF2GF چ ̈Ω ᴴ ⌧ a ℓ ̃Ω̣ ǁ ı ڭ ˜ ´ ǁ2´ ǁ چ ̈Ω ᴴ ƿ ɔ ʒ ⊃ Ṫ ў

Synchronus Rectification in DC/DC and AC/DC Converters, Isolated DC/DC Converters in Telecom and Industrial.

/ Equivalent Circuit



/ Pinning



PIN1:S PIN 2:S PIN 3 χ S PIN 4 χ G

PIN5 PIN 6 PIN 7 PIN 8:D

/ h_{FE} Classifications & Marking

ȍ M ˥ ب ˝ See Marking Instructions.

BRCS4266SC

/ Electrical Characteristics(Ta=25)

v Ϊ̣ Parameter	ɬ N Symbol	⅃ ⁝ ˣ Ţ Test Conditions	ω̣ ṇ Ö Min	Ɛ ʒ Ö Typ	ω̣ ᶻ Ö Max	D Ɉ Unit
Input Capacitance	C_{iss}	V_{DS}=30V V_{GS}=0V f=1.0MHz		1340		pF
Output Capacitance	C_{oss}			123		
Reverse Transfer Capacitance	C_{rss}			10		
Gate resistance	R_g	f=1.0MHz	0.7	1.5	2.3	Ƒ
Total Gate Charge(10V)	Q_g	V_{DD}=10V I_D=10A V_{DS}=30V		21	30	nC
Total Gate Charge(4.5V)				9.0	15	
Gate-Source Charge	Q_{gs}			4.7		
Gate-Drain Charge	Q_{gd}			2.6		

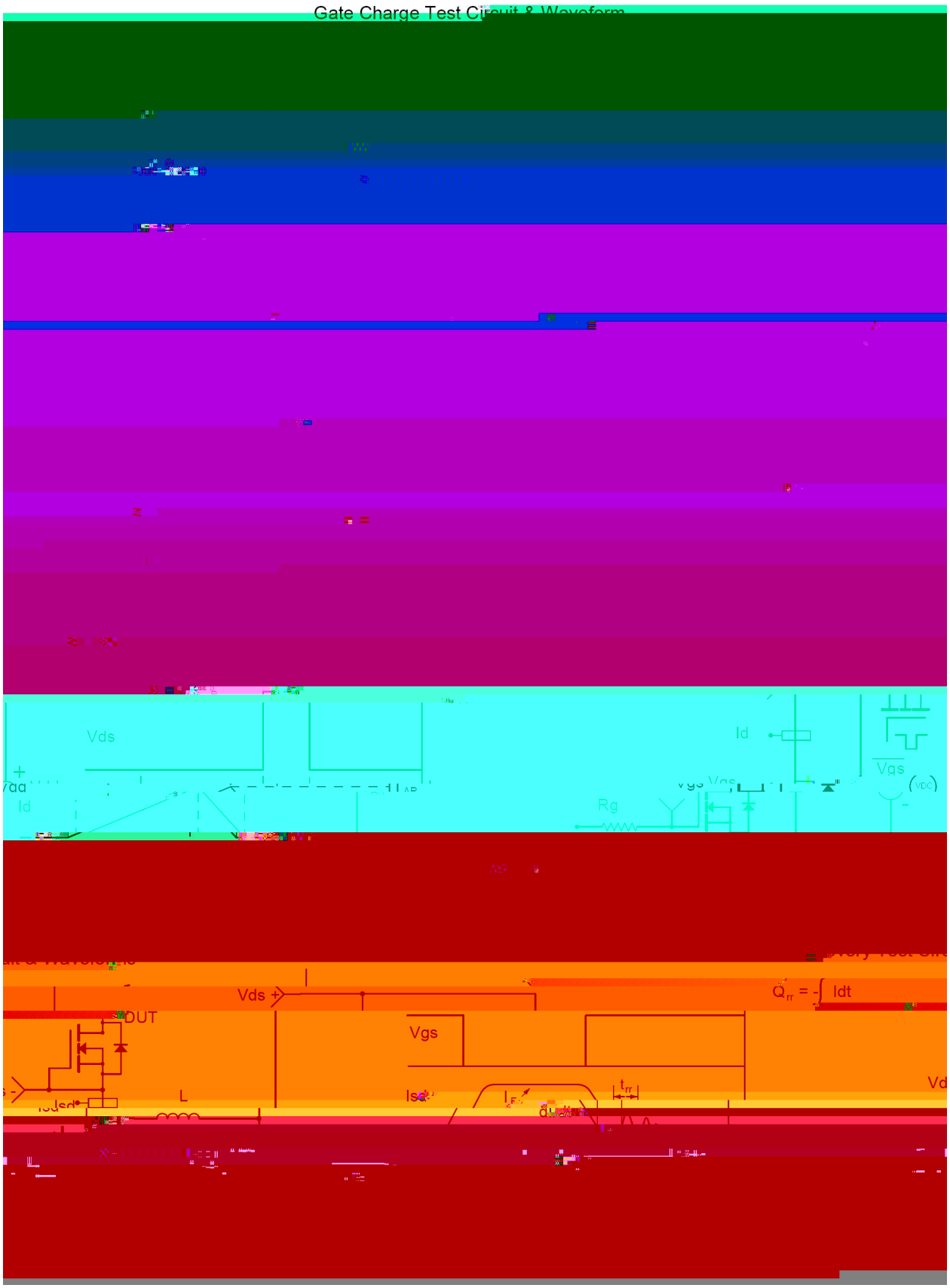
Turn-On Delay Time tRTj10.5 0 0 10.5 283.415.87042Tj7.02 0 0 Tc-0GENV

/ Electrical Characteristic Curve

/ Electrical Characteristic Curve

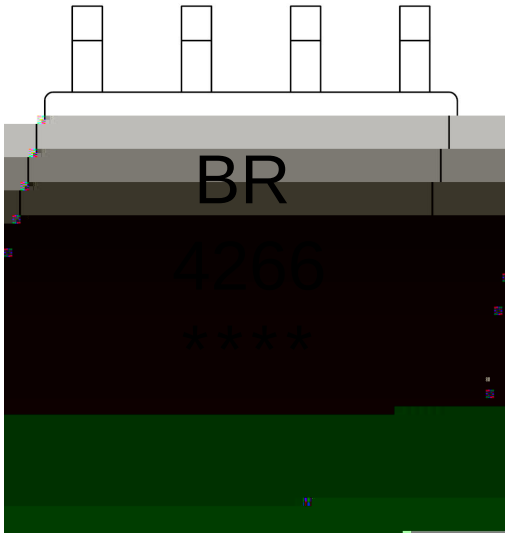
/ Test Circuit & Waveform

Gate Charge Test Circuit & Waveform



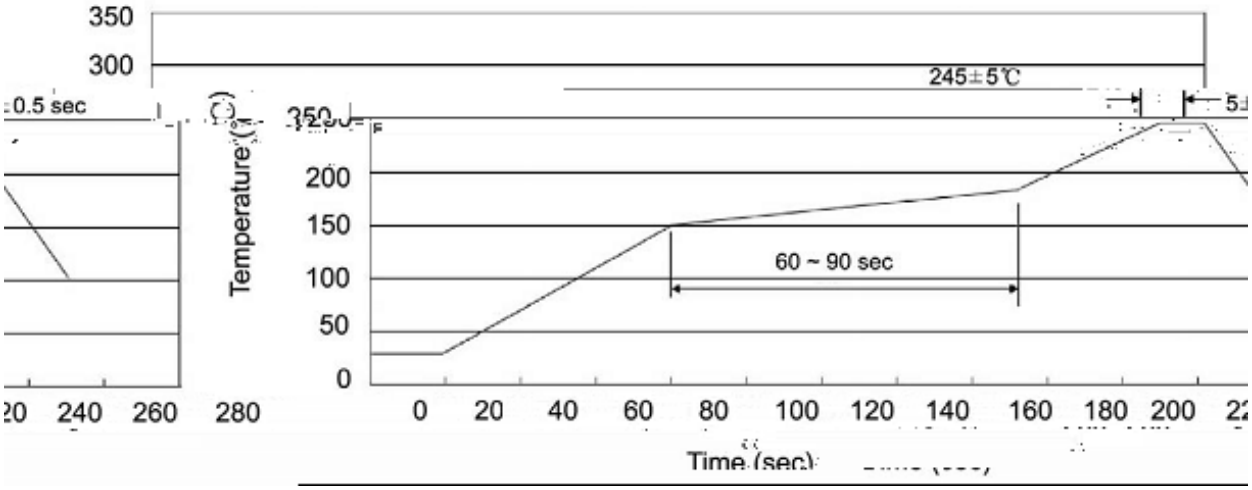
/ Package Dimensions

/ Marking Instructions



ب ˝ χ	
BR χ	ѭ ҙ ᴵ Ҧ
7599 χ	ѭ ʒ ᴺ Ҧ
---- χ	ѭ ɬ ғ ŋ̋ ᴺ Ҧ ɪ ɐ ɬ ғ ŋ̋ ᴺ ψ ק
Note:	
BR:	Company Code.
4266 χ	Product Type.
****:	Lot No. Code, code change with Lot No.

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note: