

/ Descriptions

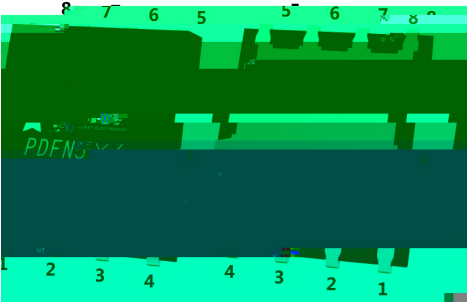
x

Ω

Ω

Ω

Ω



/ Marking

/ Absolute Maximum Ratings(T_a=25)

			±	
	≤	θ		
		θ		

/ Electrical Characteristics(T_a=25)

		μ				
					±	
		μ				
						Ω
						Ω

Ω Ω

/ Electrical Characteristic Curve

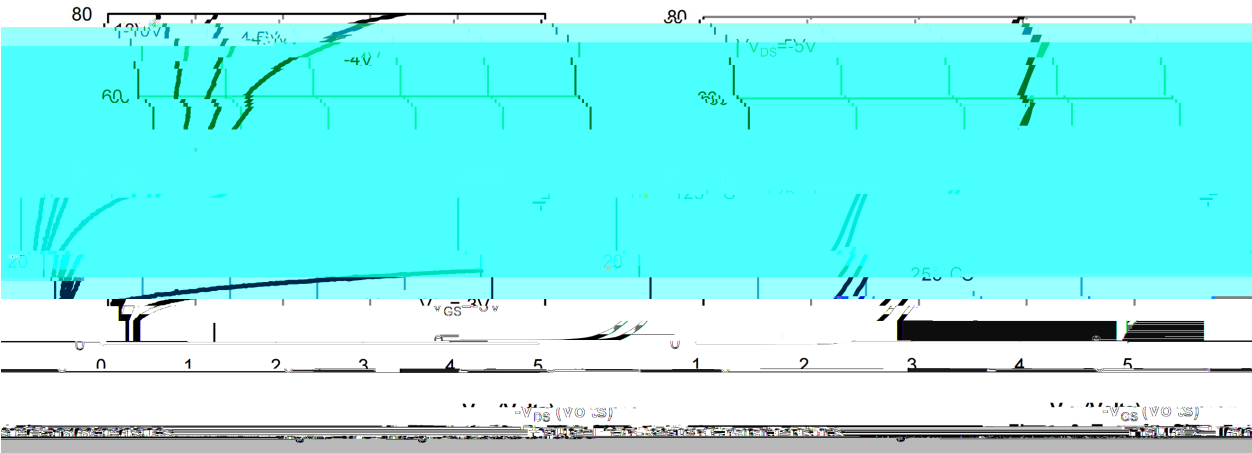
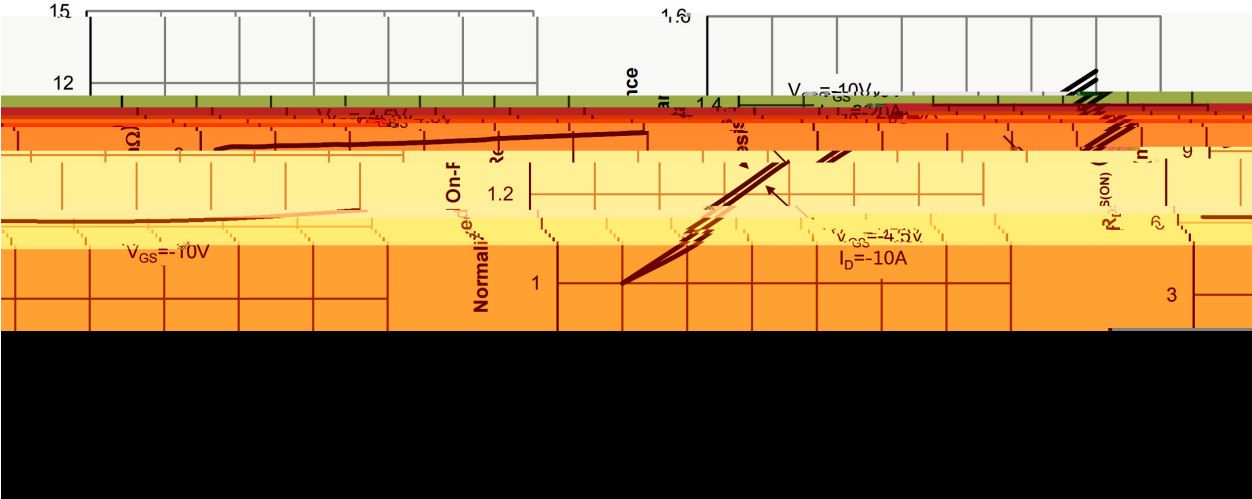
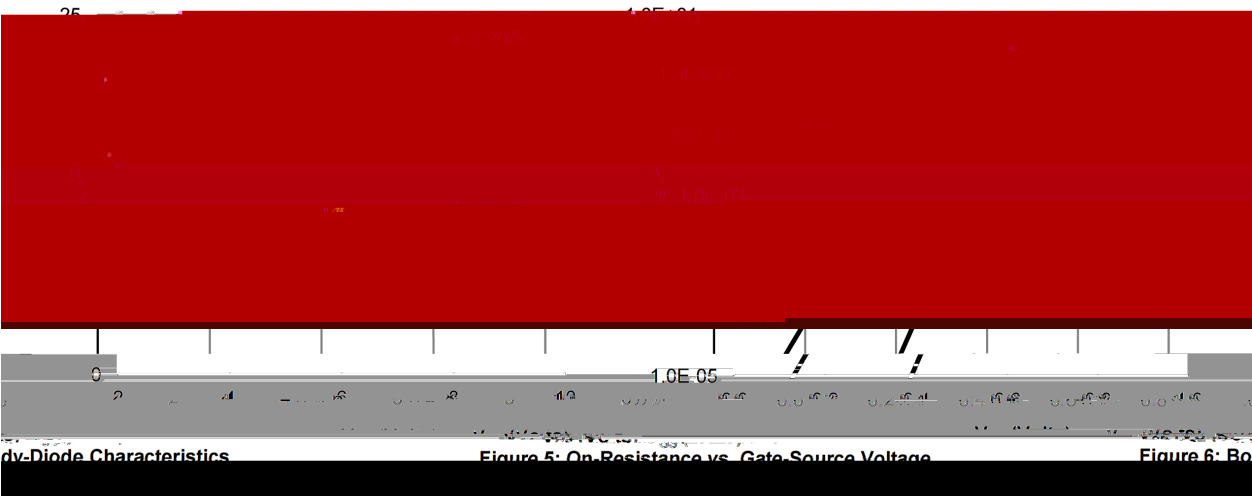


Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: R_DS(on) vs. V_DS

Figure 7: R_DS(on) vs. V_DS

/ Electrical Characteristic Curve

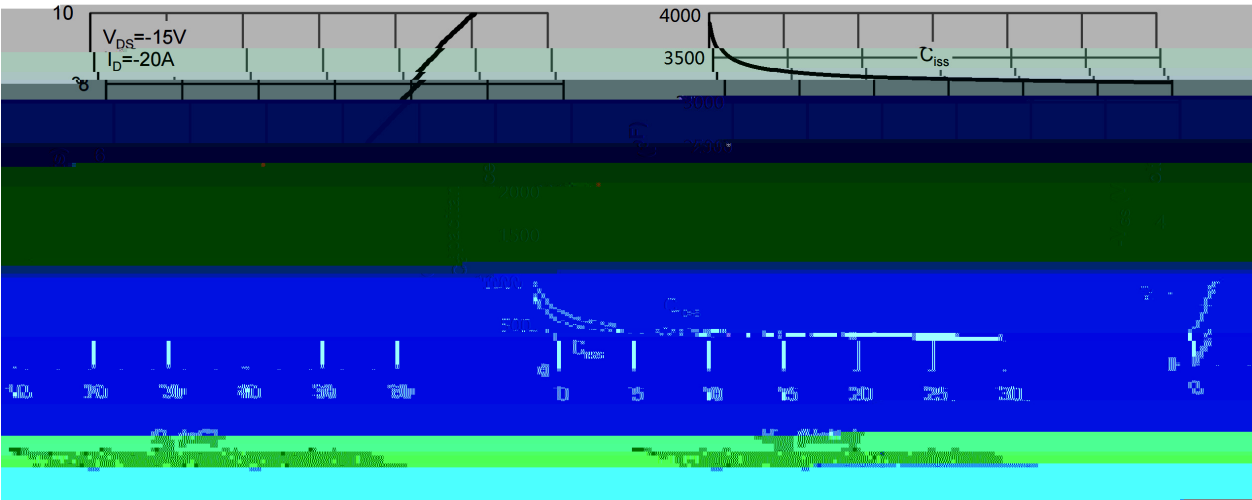
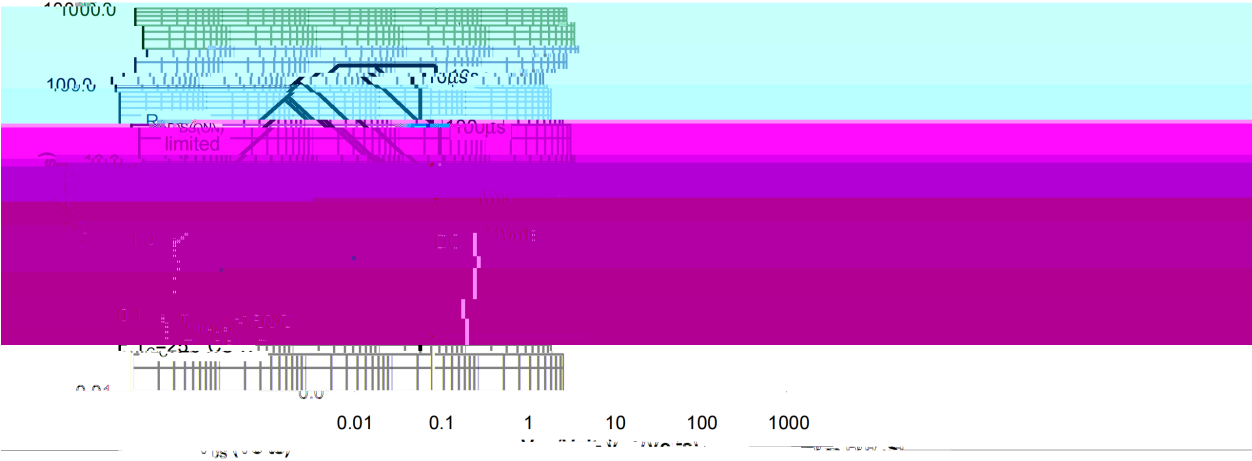
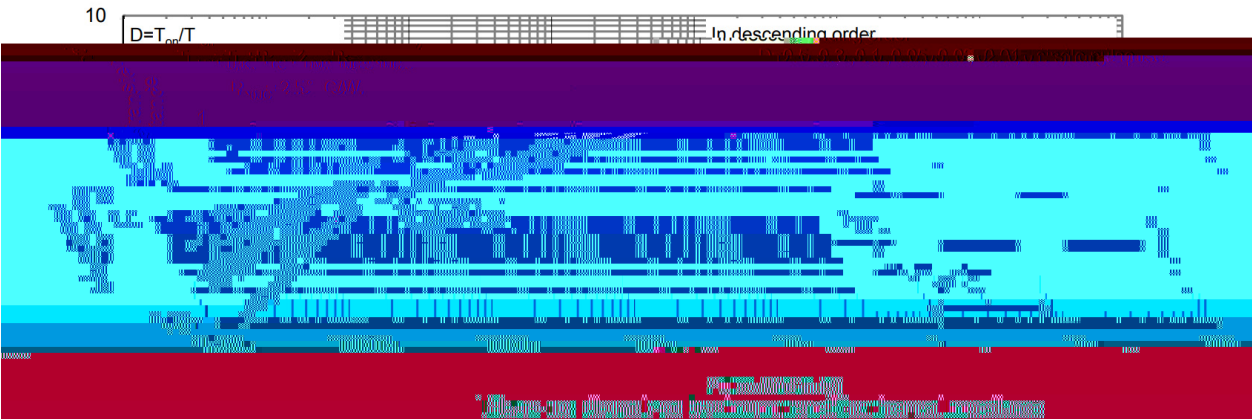
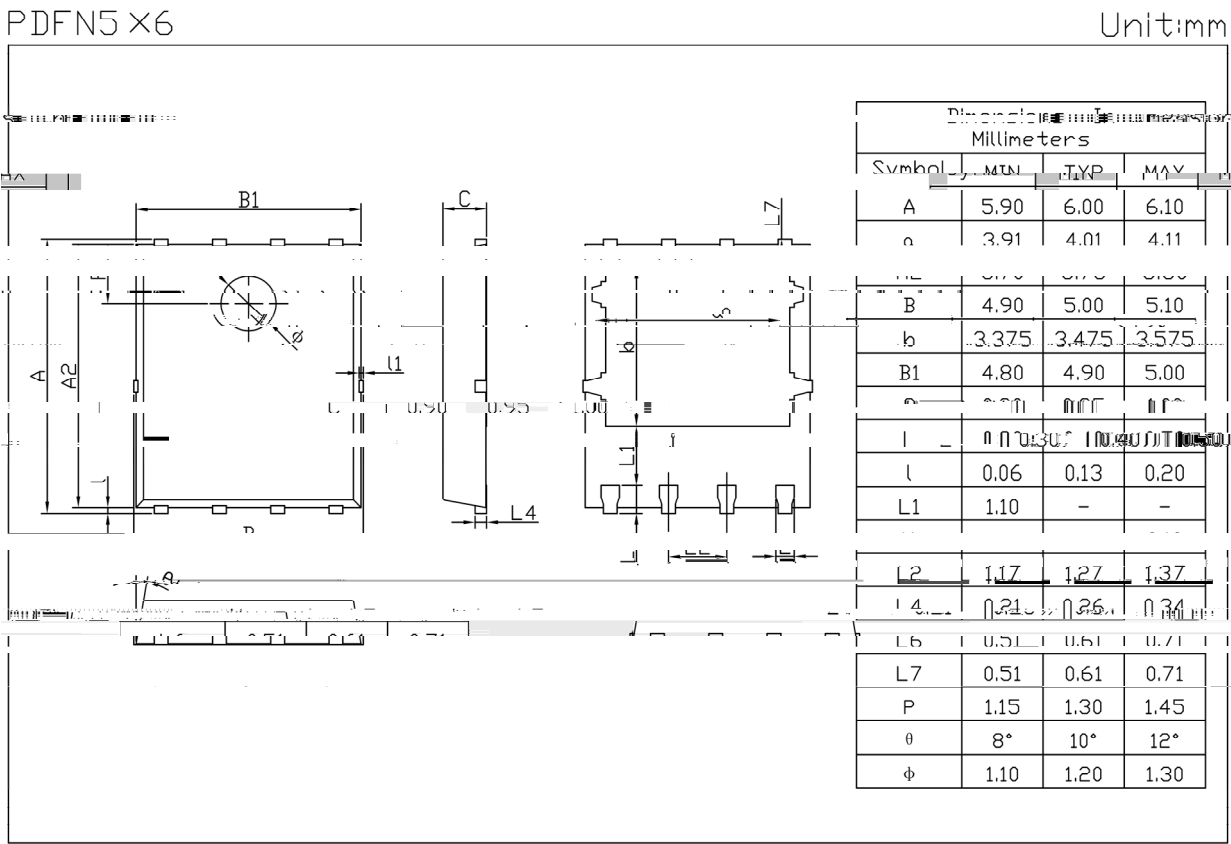


Figure 9: Maximum Forward Bias Safe Operating Area

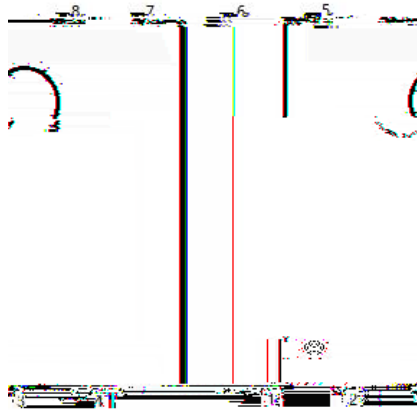


/ Package Dimensions



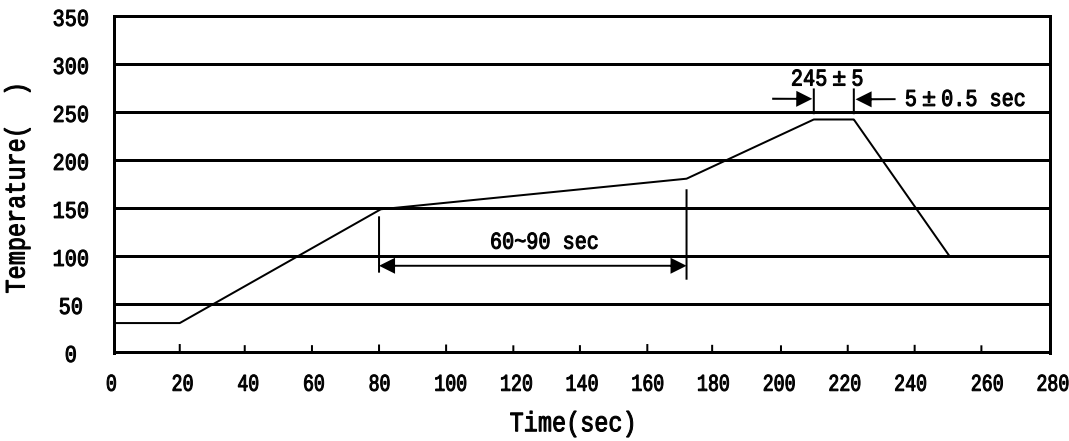
Rev.01 202209

/ Marking Instructions



BR

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

	/	/	/	/	/			

/ Notices