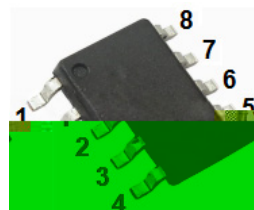


EUF P57F35VF ĩ VRS0; M t [OBJ] k 5 Nelw k t+F [OBJ] H Φ ɔ "H ɔ ɔ è ñ

ɔ ɔ ɔ ψ
 ω η Ṫ ɔ HⓅ 41:Y Ṫ ɔ p 518~818Y ʒ ω ʒ 'H ٢ ل ' 4 P k} Ṫ ɔ p 41:~518Y ʒ
 ω ʒ 'H ٢ ل ' 733Nk}ψ
 H ı ǎ FPRV'H ɿ"H ɔ ℒ 733xD ε ω ʒ ζ "H ɔ ℒ 4!9 pD ε ω ʒ ζ ψ
 A Ŀ = u ⚭ L ʒ Q"H ϕ ψ
 ° ˆ ʈ ɿ = è ψ
 ; È ` ʷ ~~W~~ Ā ψ
 è ǵ° [OBJ] ĭ ψ
 Ǧ ɾ ɹ ħ ʷ 45; ʝ ˆ° N ψ
 5 ħ'H Q ε ω ʒ 8 p v ζ ψ
 ˘ ˙ ˆ χ ɿ Ƙ"Q ʒ 433 Ƙ "Q"Q è ʒ 433 w HVG ε KEP ~~W~~ Ā ζ ĭ χ 9NY i ɹ ĭ
 χ . 20533 pD>

K 7 3 2 N 0 à ã 6 w \$ t - " ú í 3 ~ " Y 1 j 7 E 1



/ Pinning

Slq	Qdph	W sh	Ghvfufswlrq
4	H3	Lqsxw	ş x ş J
5	H4	Lqsxw	ş x ş J
6	H5	Lqsxw	ş x ş J
7	JQG	Jurxqg	ş Q ş
8	VGD	L2R	Φ`Ω`Ω 2 ş
9	VF0	Lqsxw	Φ`H ʼ ş
:	ZFE	Lqsxw	í H ş W í
;	YFF	Sr z hu	ş ʼ ş

/ Marking

ş ş ş

/ Absolute Maximum Ratings(Ta=25)

Ω	ş N	Ω	H
		ε ş	
			mA
			V

/ Reliability Characteristic

Ω	ş N	ş ş	ş ş	ε ş	ş ş	H

Ω N

/ Data Input

/ Start Condition

/ Stop Condition

The figure consists of two schematic diagrams of an experimental setup. The top diagram is a top-down view of a rectangular tank. A central horizontal channel is defined by dashed lines, with a flow arrow pointing from left to right. A central hexagonal structure is connected to a vertical line. A label 'SP1' is located on the right side. The bottom diagram is a bottom-down view of the same setup, showing a central channel and a hexagonal structure. A label 'SNA' is located on the left side.

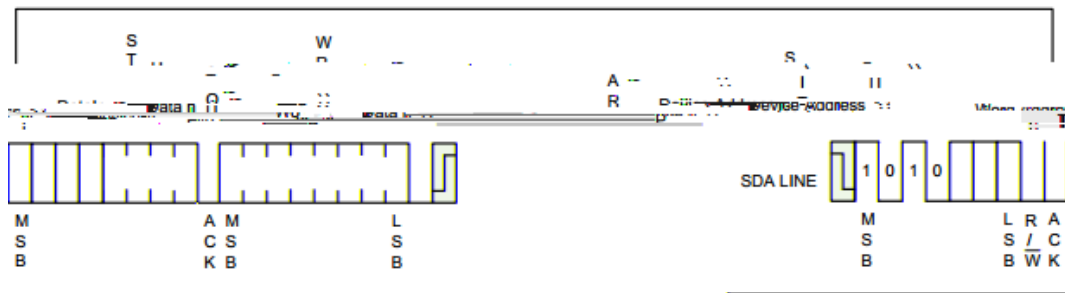
ACK / Acknowledge

5 17

/ Functional Description

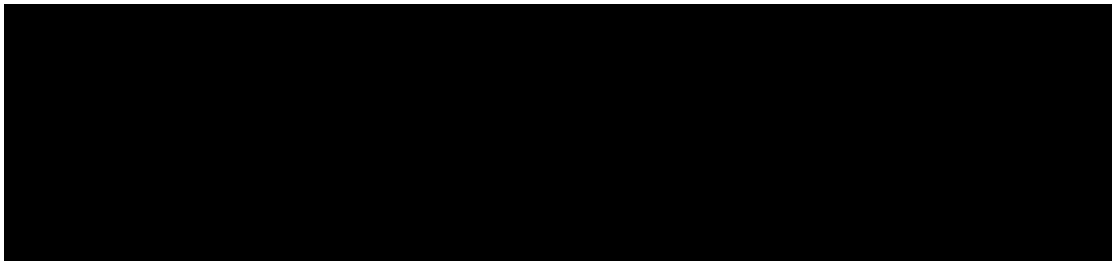
/ Functional Description

/ Functional Description



/ Page Write

ʷ = "Ḥ ɫ È ` "Ḥ ˈ ĩ ɑ p ɥ ɕ "Ḥ j ʒ̞ ʔ̚ p. n
 EUFP57F35VF ỳ ɥ È ` "Q̣Ω ɑ ɤ ɹ ɹ ʃ ˆ ʊ̯ "Q̣Ω È EUFP57F35VF ⅓ ỳ
 "Q̣Ω È ` ʁu ɪ ɒ ʒ b ʒ ɥ ɑ ɤ ʊ̯ ɪ ʔ̚ N̩̄ ɹ ʔ̚ ʷ "Ḥ



p i ò ¼ "Ω È "Ω È s x H 6 y p u u ½ "Ω È s x y j u ɸ è
 Φ y λ E u i ã È s x ʔ ʔ ʔ ʔ H = È ʔ p ã b ' ʔ
 EUF P 57 F 35 VF "Ω È λ ; "Ω È s x N ʔ "Ω È N q u "H Ω ʔ s
 x ʔ i E = ω È ʔ M È

/ Functional Description

/ Acknowledge Polling

1. 当设备收到主机的唤醒信号后，设备将进入唤醒状态，并开始发送唤醒信号。
 2. 设备在唤醒状态下，将发送唤醒信号，直到收到主机的应答信号为止。
 3. 设备在唤醒状态下，将发送唤醒信号，直到收到主机的应答信号为止。

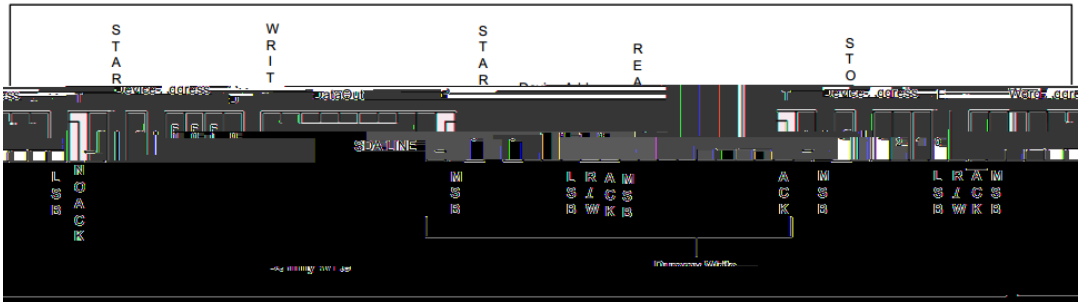
/ Write Identification Page

1. 当设备收到主机的写入识别信号后，设备将进入写入识别状态，并开始发送识别信号。
 2. 设备在写入识别状态下，将发送识别信号，直到收到主机的应答信号为止。
 3. 设备在写入识别状态下，将发送识别信号，直到收到主机的应答信号为止。

/ Lock Identification Page

1. 当设备收到主机的锁识别信号后，设备将进入锁识别状态，并开始发送识别信号。
 2. 设备在锁识别状态下，将发送识别信号，直到收到主机的应答信号为止。
 3. 设备在锁识别状态下，将发送识别信号，直到收到主机的应答信号为止。

/ Functional Description



Ilj xuh ; Udqgr p Dgguhwv Uhdg

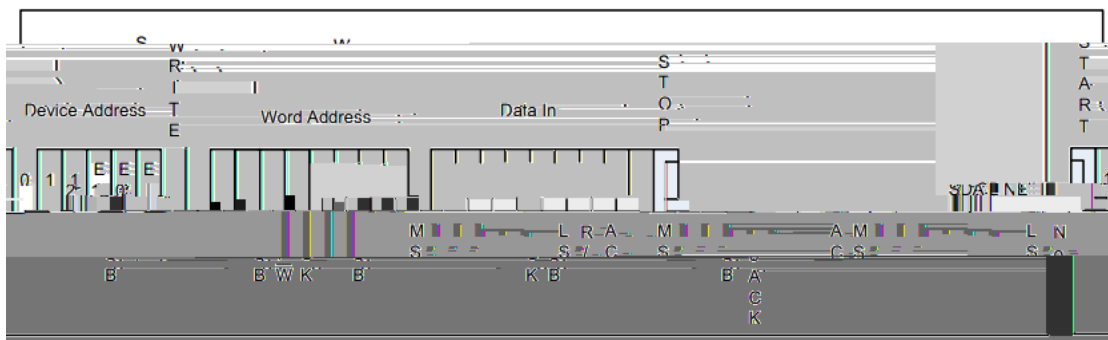
/ Functional Description

/ Read Identification Page

%V^W ε 49 È^z ĩ Ĵ ĩ^W = Q p c W Ā ĩ X ⇌ ĵ ĥ V^W = Q ū c V^W
 = "Y ŋ c Ī "Y ŋ ĩ ŋ c ŋ ε ĩ s è Ķ° ζ ŵ [OBJ] p ĩ Ā Φ ĩ 1 3 %V^W ĩ ĥ Θ
 4344e ś x ħ D: 2 D9 ō ε 3 ā D8 Y ś x ħ D7 2 D3 ĥ Θ %V^W [OBJ] È^z ś x LG^W =
 l c [OBJ] È^z "Q j ê λ^W = ĵ ģ ε b ħ J 43g c %V^W H È^z "Q Z η ā v 9 + LG^W
 Ĵ ģ 49 È^z ζ

/ Read the Lock Status

Q ū c Φ ĩ Ĵ = ĥ A Ā ŋ [%V^W "Y ŋ. "Q Ŵ È^z] ĩ s ĩ %V^W = [OBJ] ĵ ĥ 2 J ĵ + ð
 b ' V^W = ' ĵ ĥ ĩ Φ ĩ ε + DFN b ' V^W = q ĵ ĥ ĩ Φ ĩ ε + Q r DFN + b g 43 "A~ ,

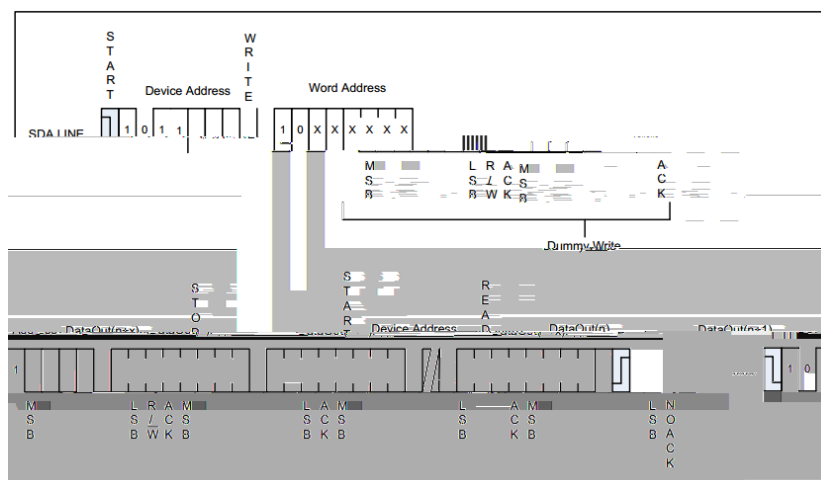


/ Read Serial Number

%V^W ε 49 È^z ĩ Ĵ ĩ^W = Q p c W Ā ĩ X ⇌ ĵ ĥ
 c ž° N 1 ĩ ž c ž° ĩ l ĩ [OBJ] 4 "A~ [OBJ] ś x ū O Q ĩ = ĥ [OBJ]
 È^z ś x ō ε ž° N e [OBJ] V D ś x c "Q 45; ħ Q - [OBJ] λ N
 ' [OBJ] ś x "Y ĩ p W h HHSUR P Ķ° ž° N e ĵ ĩ % Z "E Φ D b ž°
 c ā ĩ ž c p [OBJ] ŋ Q ś x "Y ĩ Φ J ĵ b ' [OBJ] ĥ ĩ HHSUR P Ķ° ž° N e ĩ
 "H Y E ś x c [OBJ] ś x "Y ĩ N p ū ĩ [OBJ] ω ħ J ĩ ŋ ĩ 4 ž° N e [OBJ] M ś x Q ū [OBJ] ħ
 J c ž° N M ĩ ĩ [OBJ] ž° N j

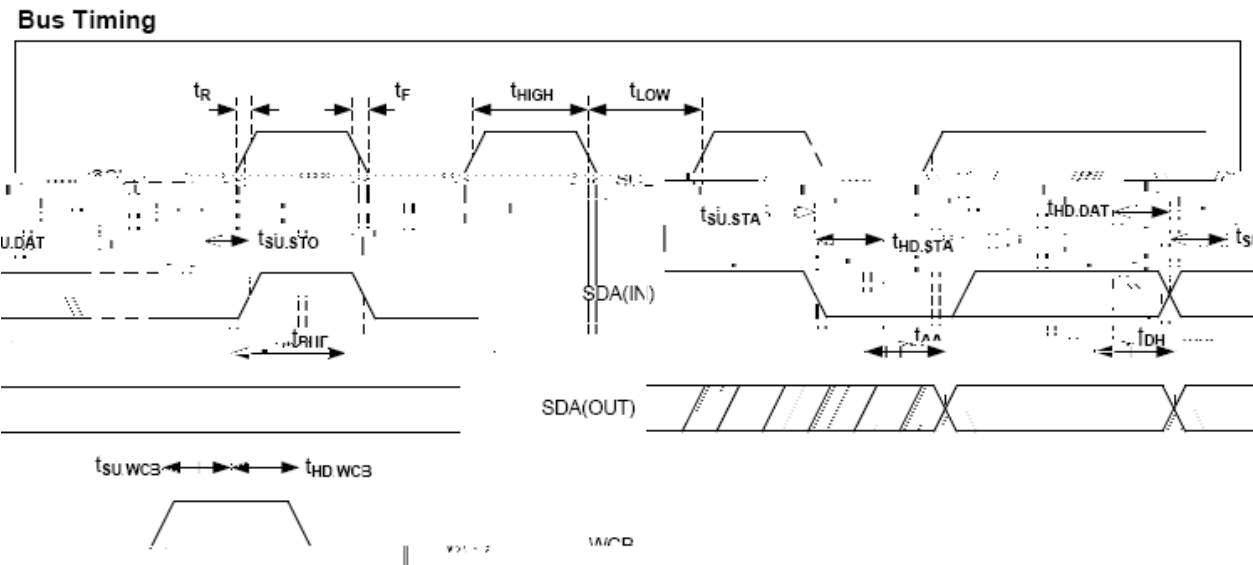
/ Functional Description

% 0 2° N 0000 È s x D: D9 H 2° 043 b b 5 "A~ 0000 l f s x b ' i 043 b 0 0000
 È s x 0 p f t N j ' h 0000 "Q"Q ε~ x b ' z i y z U'Q z 2° N 0000 M È ' È s
 x j x l ;3k z
 E j 45; H ε 49 È "Q"Q z 2° N 0000 P H l l z "E r è N M7 p 0000 2° N "Q"Q z
 "Q"Q È s x N z t 45; 0000 Å H 2° N È s x E i i ≡ ω z ε DFN z H 2° N z H
 l B X B ε 0 g : z



Il j xuh 44 Vh t xhqwldo Uhdg

/ Time Sequence Diagram



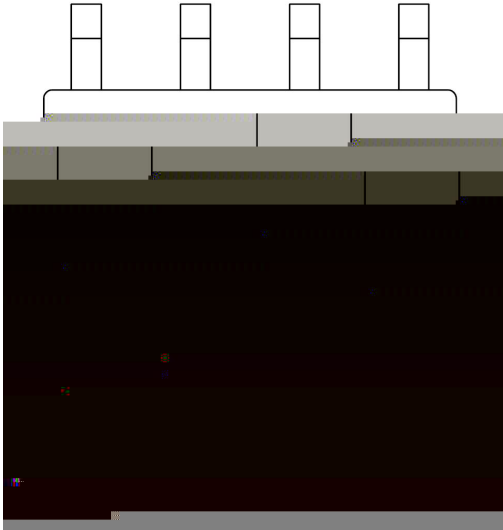
Write Cycle Timing



/ Package Dimensions

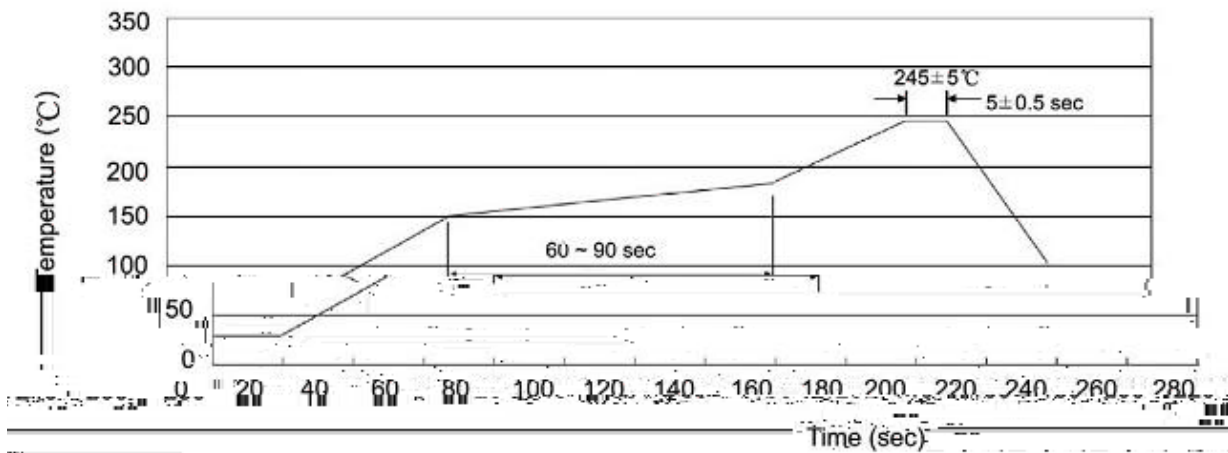


/ Marking Instructions



б " χ	
χ	ⁿ Γb
57F35 χ	з ⁿ Γb
---- χ	ι ḡ ⁿ Γb ⚡ ι ḡ ⁿ

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



ب"خ

İ ♪ □ ŷ

Hj

$$\check{S} \quad \square \quad \check{y}$$

‘H j ‘Y A

ì ʏ ɔ ɤ ʝ

Y / Resistance to Soldering Heat Test Conditions

U b v 0

○

○

、 上

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box
SOP/ESOP-8	4,000	2	8,000	6	48,000	13 ×12	360×360×50	380×335×366×12