

/ Descriptions

> J K L 8 M N N . < O < P Q R S T U V
Silicon PNP transistor in a SOT-89 Plastic Package.

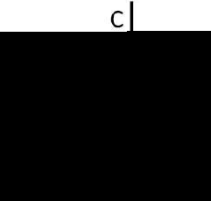
/ Features

W X Y Z [\] 0 \$ %) * V
Low $V_{CE(sat)}$, high current, HF product.

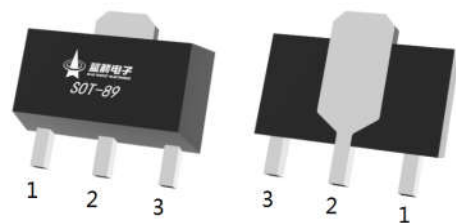
/ Applications

^ ; _ ` a b c d e f g h i j k l m n o c d \ p V
General purpose switching and muting, LCD back-lighting, supply line switching circuits.

/ Equivalent Circuit



/ Pinning



PIN1 q Base PIN 2 q Collector PIN 3 q Emitter

/ Marking

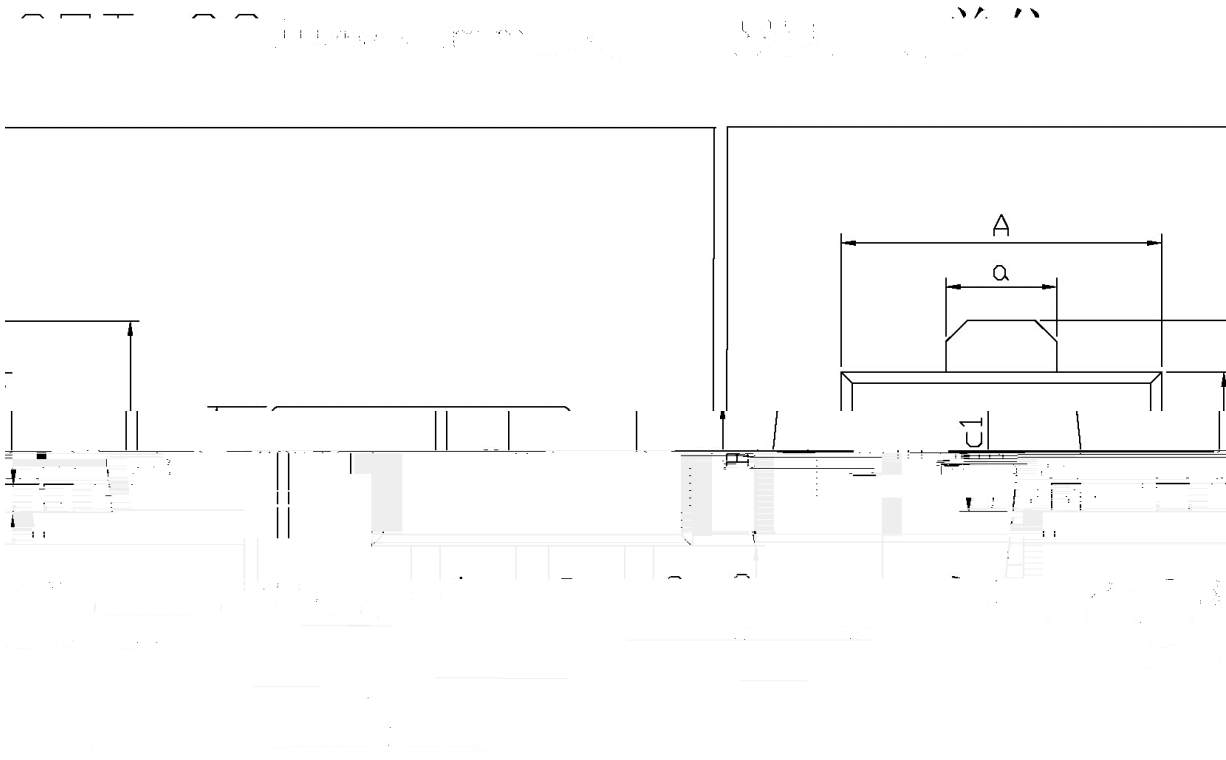
Marking	HS46 * *
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r s Parameter	t u Symbol	s v Rating	w x Unit
Collector to Base Voltage	V _{CBO}	-50	V
Collector to Emitter Voltage	V _{CEO}	-50	V
Emitter to Base Voltage	V _{EBO}	-5	V
Collector Current - Continuous	I _C	-3	A
Peak collector current	I _{CM}	-5	A
Collector Base - Continuous	I _B	-0.5	A
Total power dissipation _(y)	P _{tot(y)}	550	mW
Junction Temperature	T _j	150	
Storage Temperature Range	T _{stg}	-65 150	

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r s Parameter	t u Symbol	f f z Test Conditions	... 1 v Min	† ‡ v Typ	... 0 v Max	w x Unit
Collector Cut-Off Current	I _{CBO}	V _{CB} =-50V I _E =0			-100	nA
Collector Cut-Off Current	I _{CBO}	V _{CB} =-50V I _E =0 T _j =150			-50	μA
Collector cut-off current	I _{CES}	V _{CE} =-50V V _{BE} =0			-100	nA
Emitter Base Cut-Off Current	I _{EBO}	V _{EB} =-5.0V I _C =0			-100	nA
		h _{FE(1)}				
		V _D				

\$ % & " / Package Dimensions



Symbol	Dimensions In Millimeters	Symbol	Dimensions In Millimeters
	5.8 ± 0.1		4.4 ± 0.1
	0.8 ± 0.1		1.2 ± 0.1

BR3CG5350TA

Rev.A Sep.-2022



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