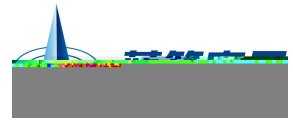


F	2017-4				
G	2020-4-21	2	IEBO ICEO		

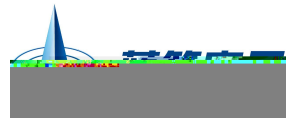
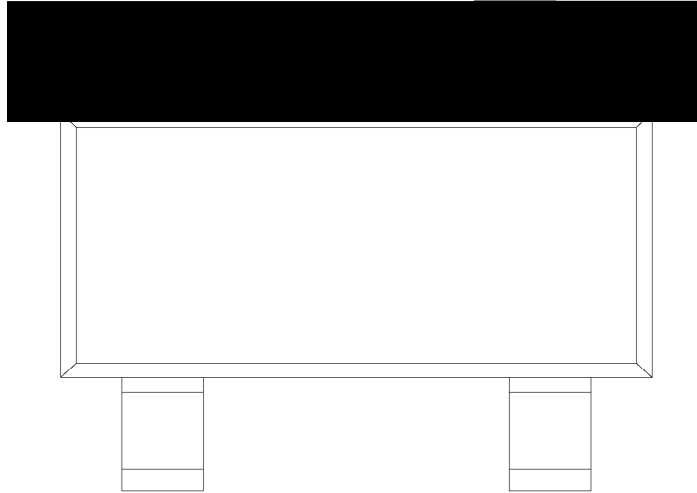


SOT-23

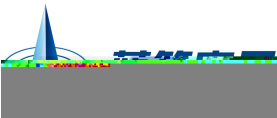
NPN

Silicon NPN transistor in a SOT-23 Plastic Package.

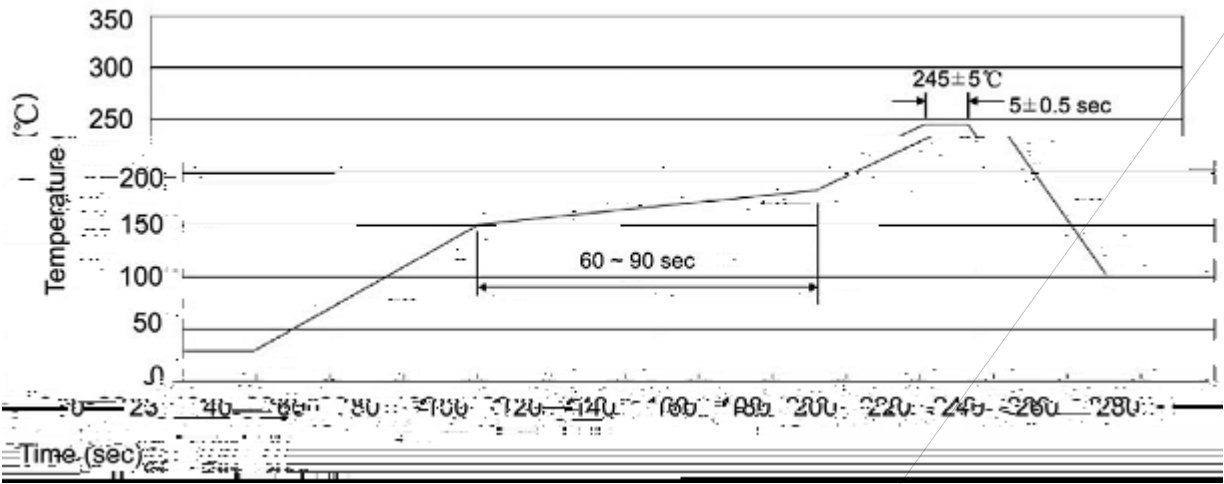
Low current, Low voltage.



H
1J



Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-----|-----|----|---------|--------------------------------------|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245 | 5 | 5 | 0.5sec; | 2.Pea i |