/ Descriptions

WR0<5+U, ṂṂÞ QSQ ÆṀ ч s ‖ ㄱ Silicon NPN transistor in a TO-92(R) Plastic Package.

/ Features

ᗅ Ɔ Я ı Ӈ ƍ ϕ

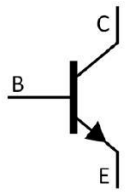
High Voltage, Low Noise.

/ Applications

ᵹ ˇ ѽ ᶻ ȝ Ầ Ԋ

General purpose and switching application.

/ Equivalent Circuit



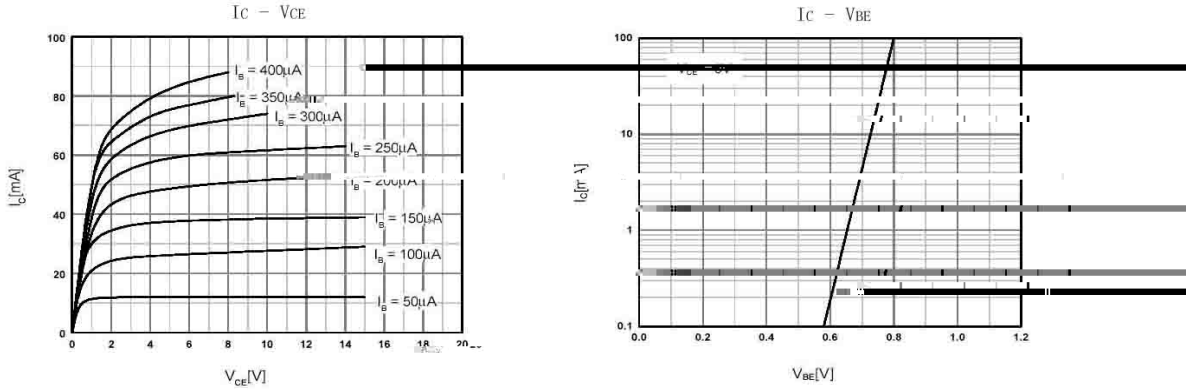
/ Pinning

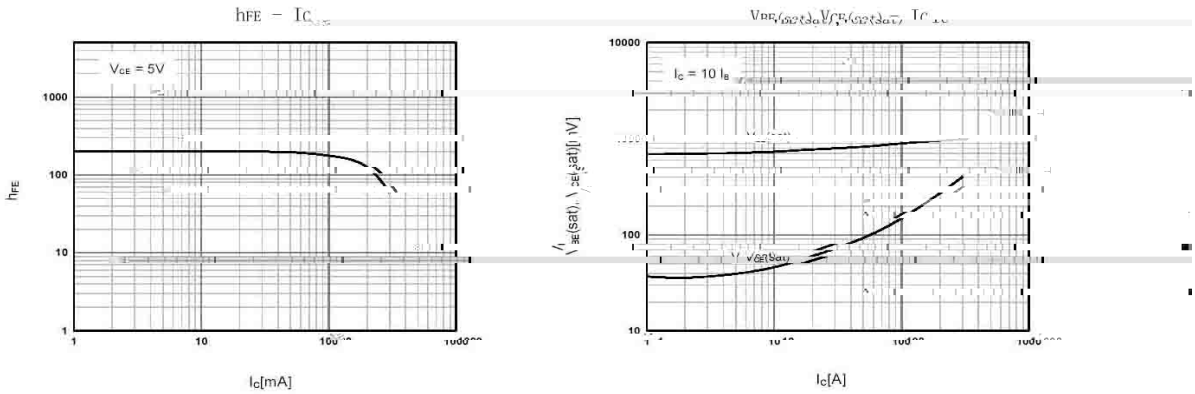
PIN1 χ Emitter PIN 2 χ Base PIN 3 χ Collector

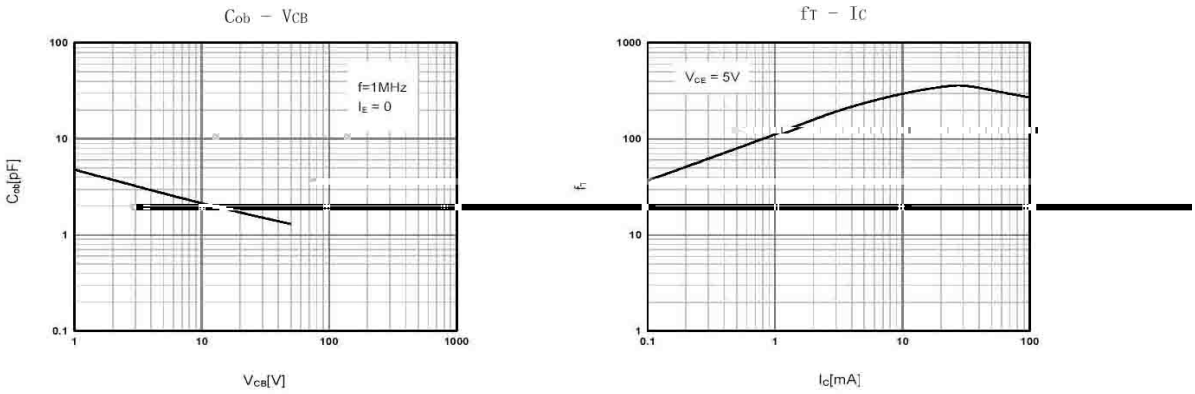
/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	A	B	C
h_{FE} Range			

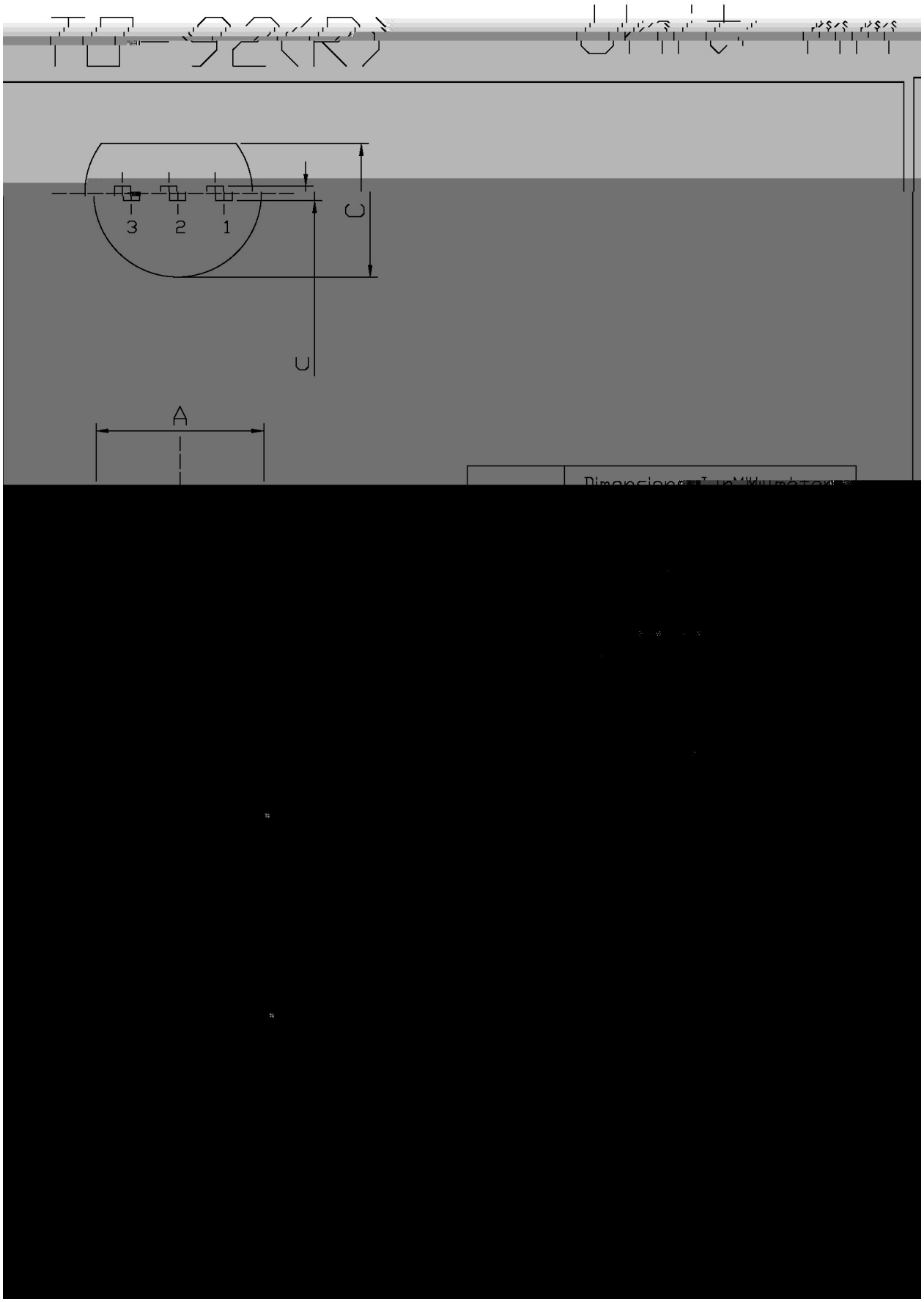
/ Electrical Characteristic Curve

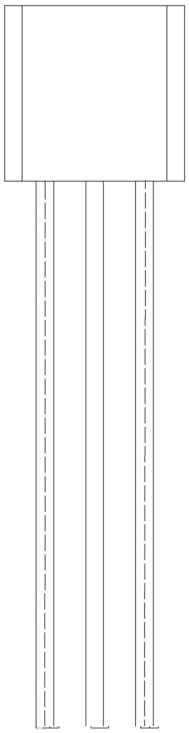




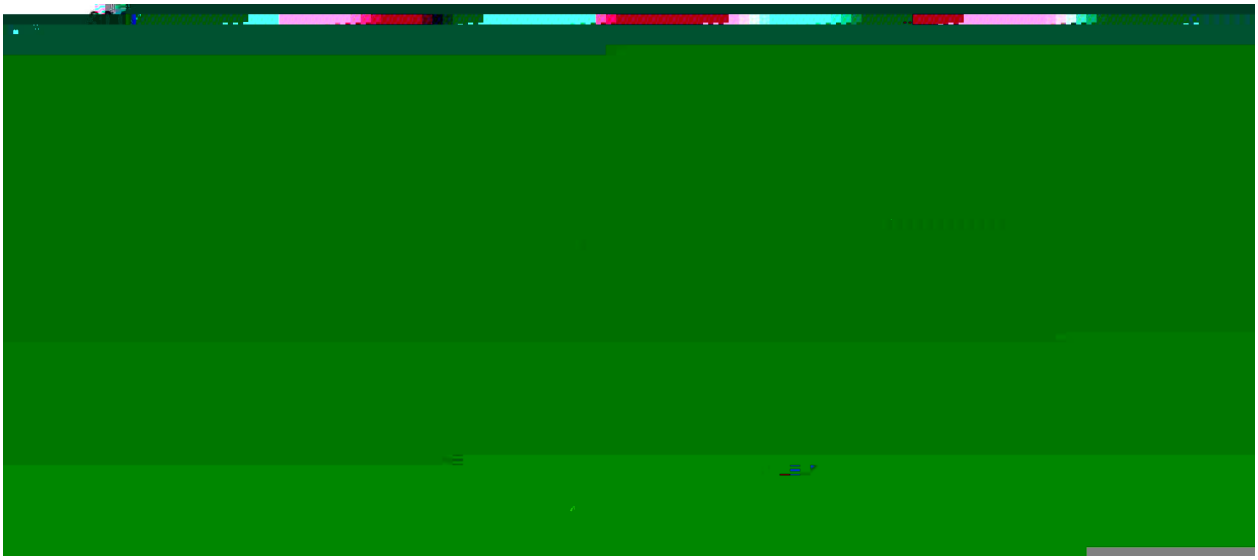


/ Package Dimensions





( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ł ı `H ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ł ı `H ڑ `Y ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ｲ ˛ ɔ ﻉ ỷ ㎛ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ł `H ڑ χ 10 ° 1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

¨Ǫ ţ ｙ Ł / BULK

Package Type	Units					Dimension		(unit mm3)

ﾊ Ⱶ ｙ Ł / AMMO

Package Type	Units					Dimension	(unit mm3)

/ Notices