VRW0;< ṂṂÞ QSQ ÆṀч s Ⅱㄱ Silicon NPN transistor in a SOT-89 Plastic Package.

ⷯ ề ļ ' ゃ ı ɔ я ゃ/Ṃ Þ ṇ/ лъ 5VD4534D Ӈ ´

High f_T, high V_{CEO}, small flat package, complementary pair with 2SA1201A.

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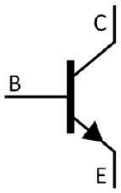
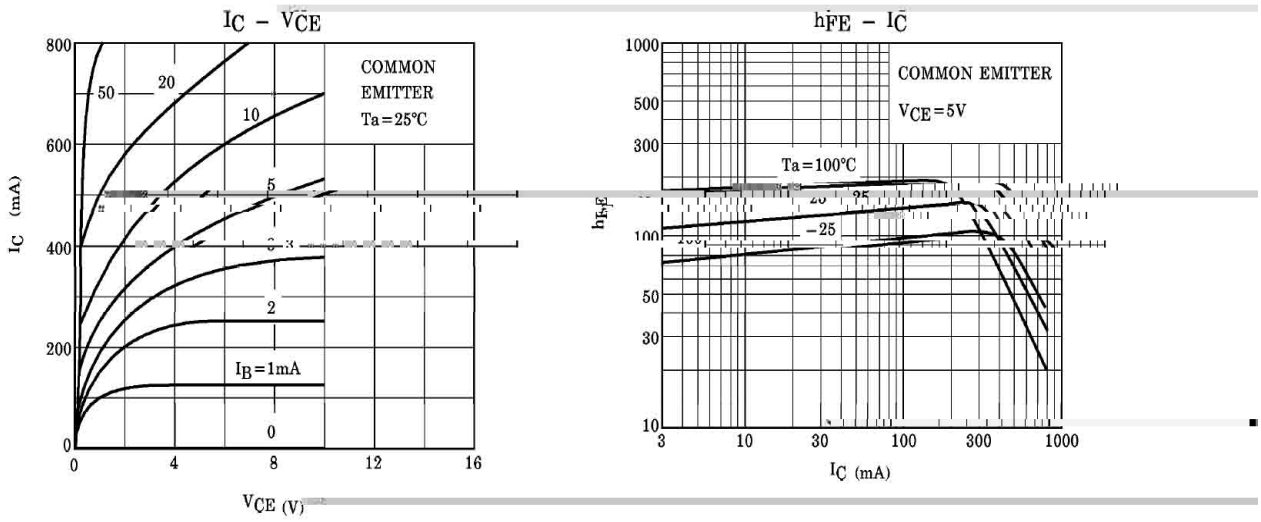
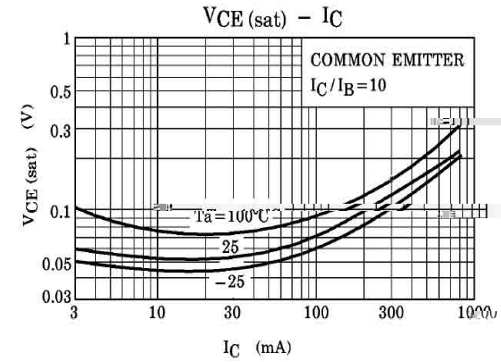
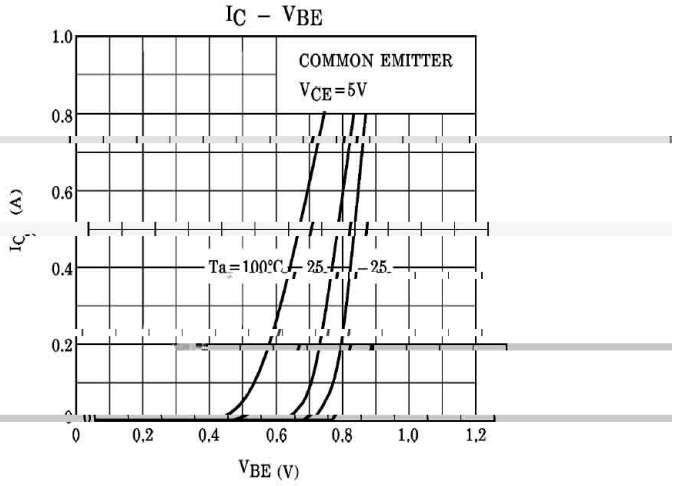


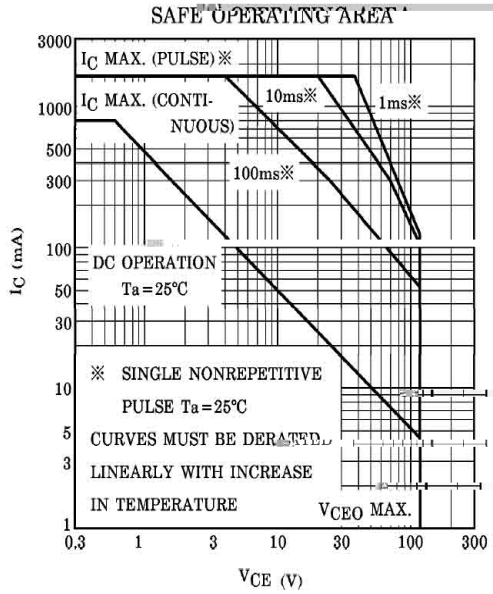
v ˝Ω Parameter	ϒ N Symbol	˝Ω Ö Rating	D Ч Unit
Collector to Base Voltage	V		

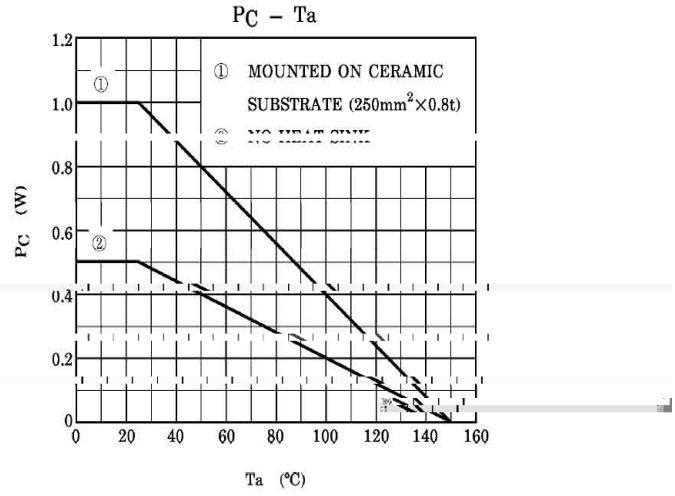
≠ ∃ Ã´E ∀ ƒ / Electrical Characteristic Curve







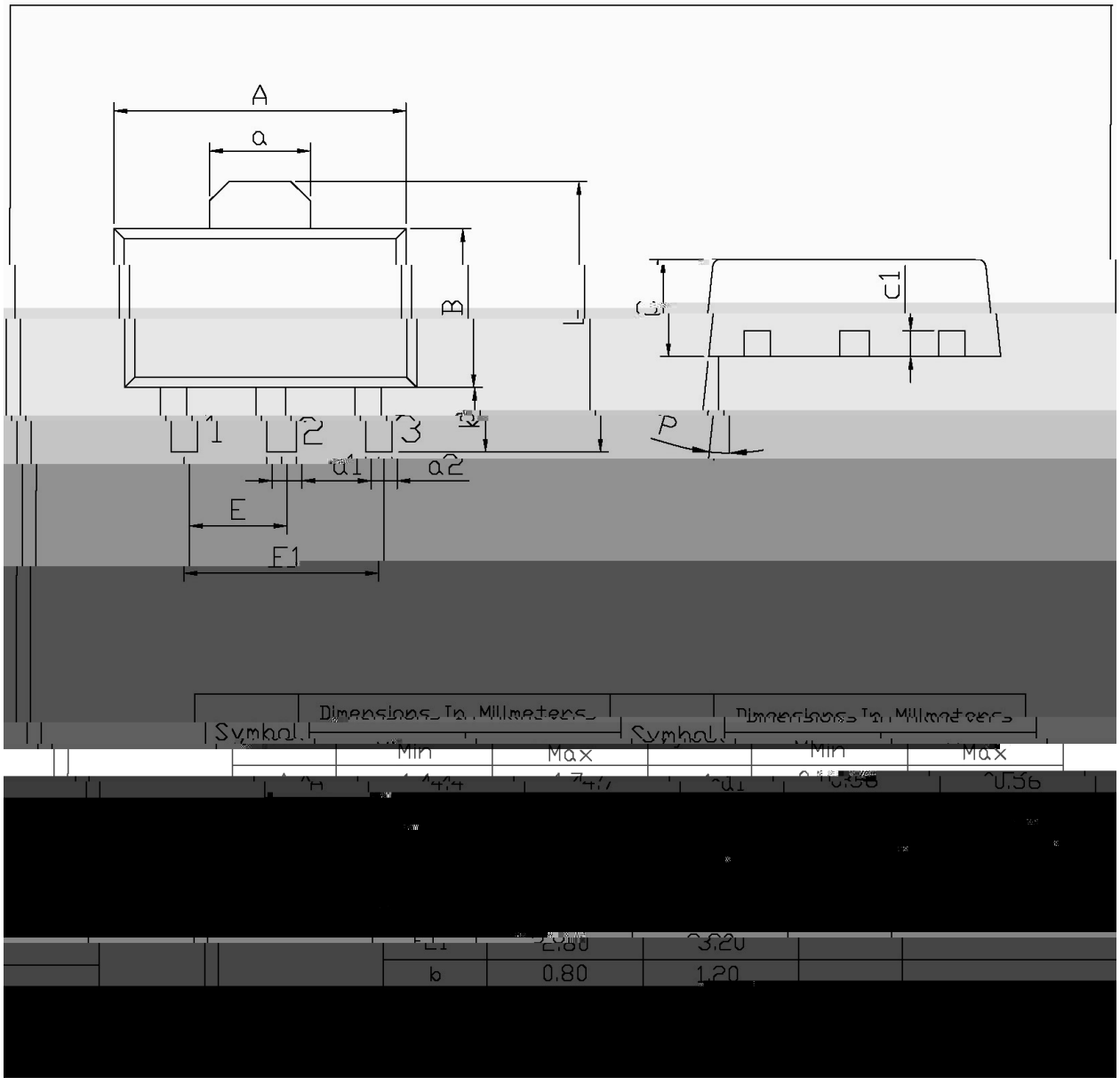




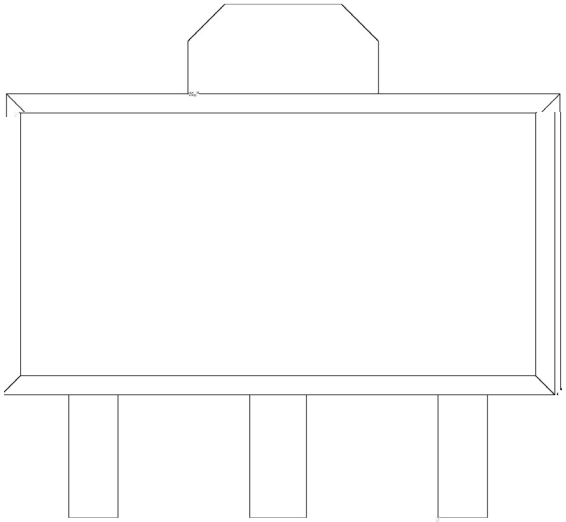
ᵘ Ė Ồ Ļ ƒ / Package Dimensions

SOT-89

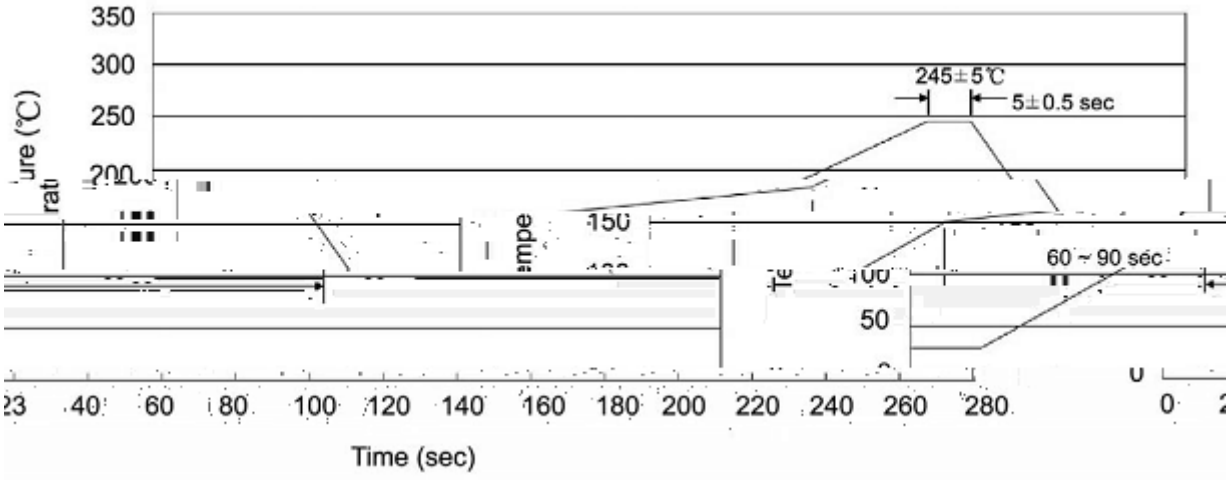
单位：mm



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	1.4[illegible]	1.7[illegible]	d1	[illegible]	0.36
E1	2.[illegible]	3.20			
b	0.80	1.20			



⁄ ƒ ╕ẃ´E ⅄ ƒ (ώ ѕ) / Wh p shudwxuh Suriloh iru LU Uhio r z Vroghulq j+Se0luhh,



ب ˝ χ

1 Ĺ ♬ □ ẙ 25 Ќ 150 ł ı `H ڑ 60 Ќ 90sec;

2 Š Ө □ ẙ 245 ° 5 ł ı `H ڑ `Y ⁄ ѭ 5 ° 0.5sec;

3 ì ˙ ⅄ ˛ ɔ ѧ ẙ ѭ 2 Ќ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:245 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

⁄ ü q ¯ `Y Ţ / Resistance to Soldering Heat Test Conditions

□ ẙ χ 260 ° 5 ł `H ڑ χ 10 ° 1 sec. Temp.:260±5 Time:10±1 sec

ɒ Ł ≮ — / Packaging SPEC.

ɔ ` ɣ Ł / REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm^3)		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

ä ѵ Ļ Ѿ / Notices