

٢٠٠ / Descriptions

SOT-89 PNP Silicon PNP transistor in a SOT-89 Plastic Package.

• ٢١ / Features

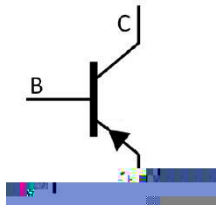
2SD2185

Low $V_{CE(sat)}$, complementary to 2SD2185.

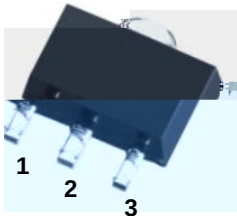
٢٢ / Applications

For low frequency output amplification.

٢٣ ٢٤ = ٢٥ / Equivalent Circuit



٢٦ ٢٧ ~ / Pinning



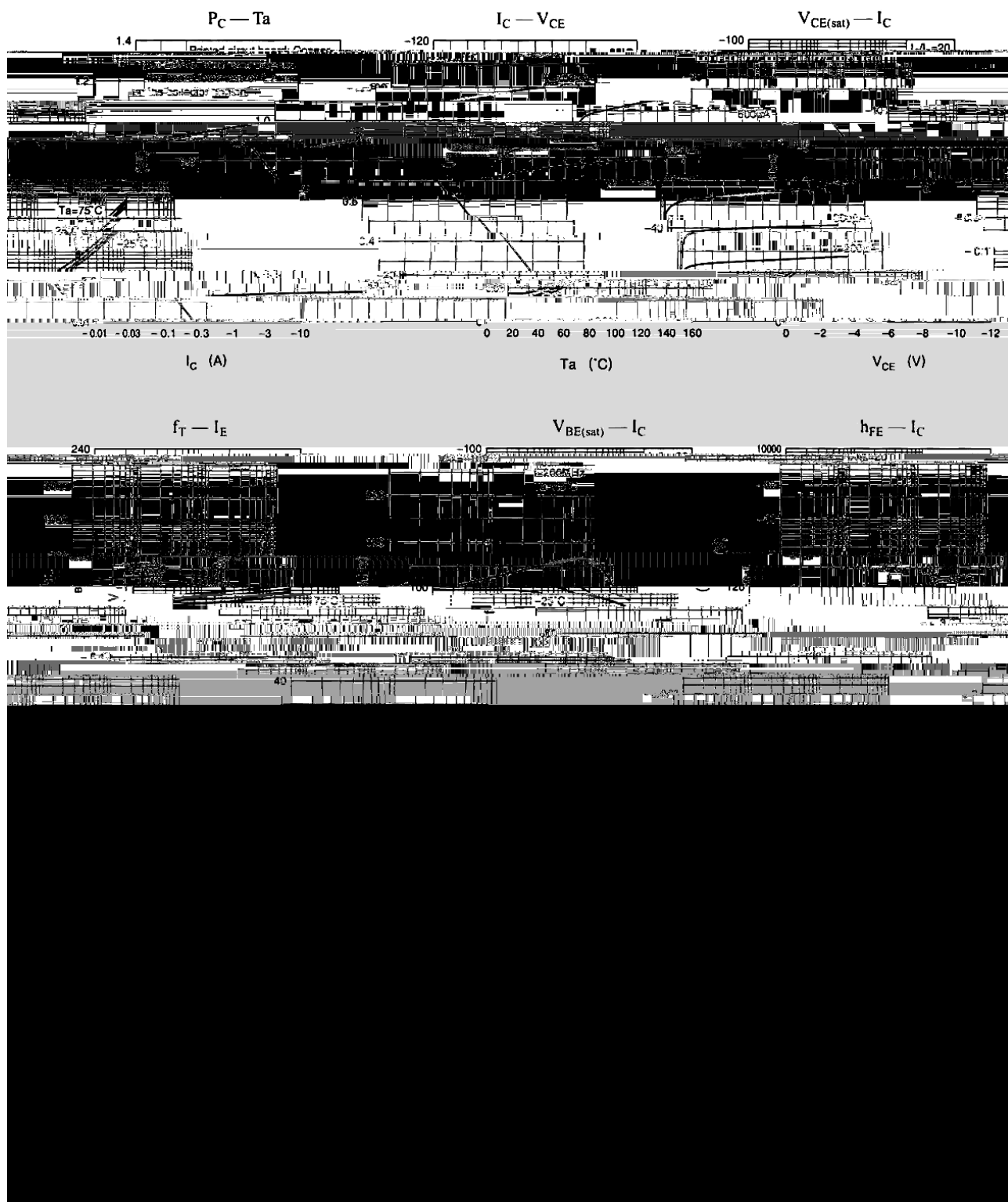
PIN1 Base PIN 2 Collector PIN 3 Emitter

٢٨ ٢٩ ~ / Marking

h_{FE} Classifications Symbol	R	S
h_{FE} Range	120 240	170 340
Marking	H1IR * *	H1IS * *

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-50	V
Collector to Emitter Voltage	V_{CEO}	-50	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current-Continuous	I_C	-2.0	A
Collector Current -Continuous(Pulse)	I_{CP}	-3.0	A
Collector Power Dissipation*	$*P_C$	1.0	W
Junction Temperature	T_j	150	

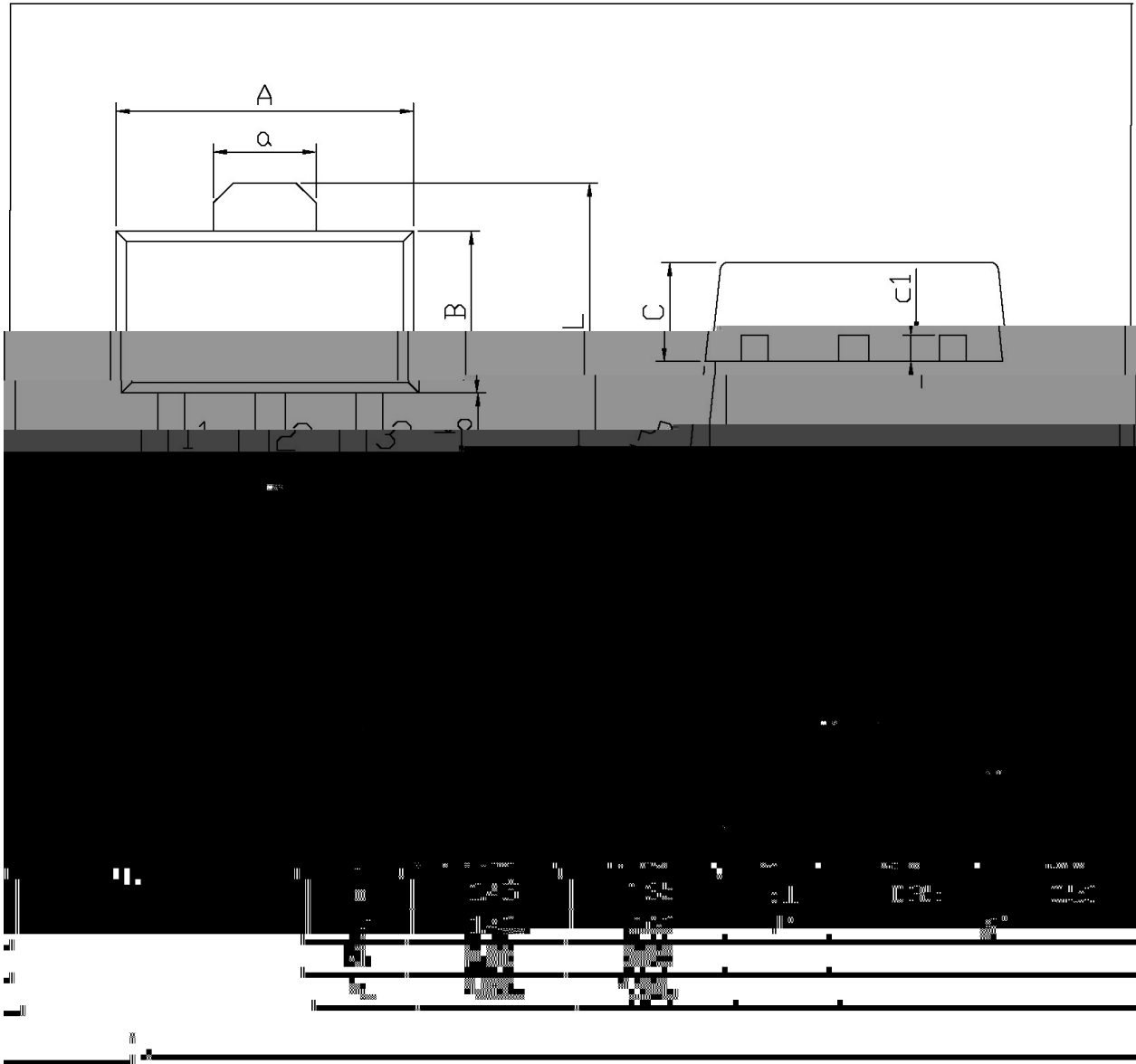
电 气 特 性 曲 线 / Electrical Characteristic Curve



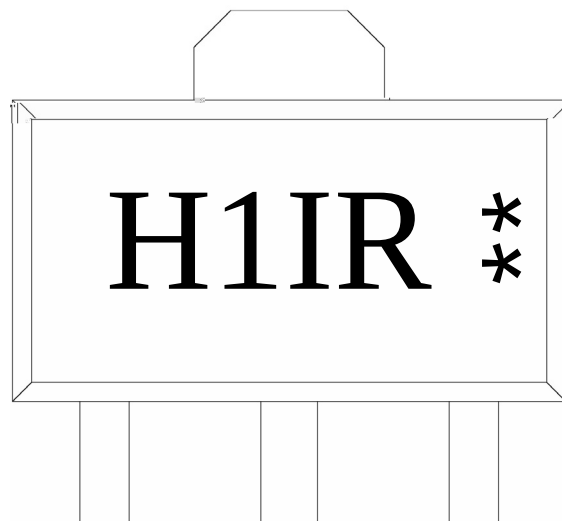
⌘ È Ò Ì f / Package Dimensions

SOT-89

单位: mm



1. 1 / Marking Instructions



H

1I

R: h_{FE}

**

Note:

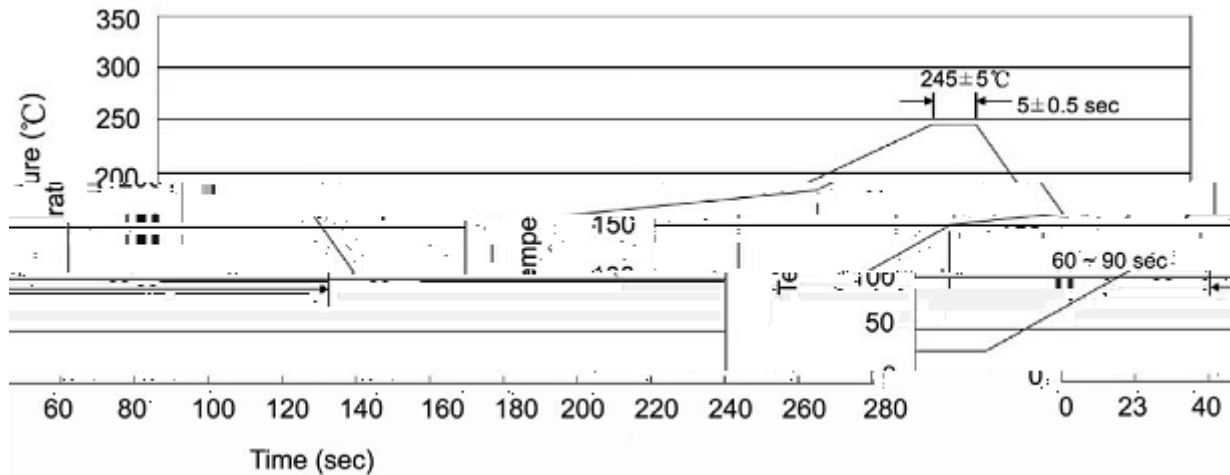
H: Company Code.

1I: Product Type.

R: h_{FE} Classifications Symbol

**: Lot No. Code, code change with Lot No.

Whp shudwxuh Suriloh i ru LU Uhior z Vroghulq j+Se0l uhh,



Note:

- | | | | | | |
|---|-------|-----|-----------|---|--------------------------------------|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245±5 | | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. | |
| 3 | | 2 | 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

Resistance to Soldering Heat Test Conditions

260±5 10±1 sec. Temp.:260±5 Time:10±1 sec

Packaging SPEC.

/ REEL

Package Type	Units					Dimension (unit mm ³)		

Notices